

SHEET

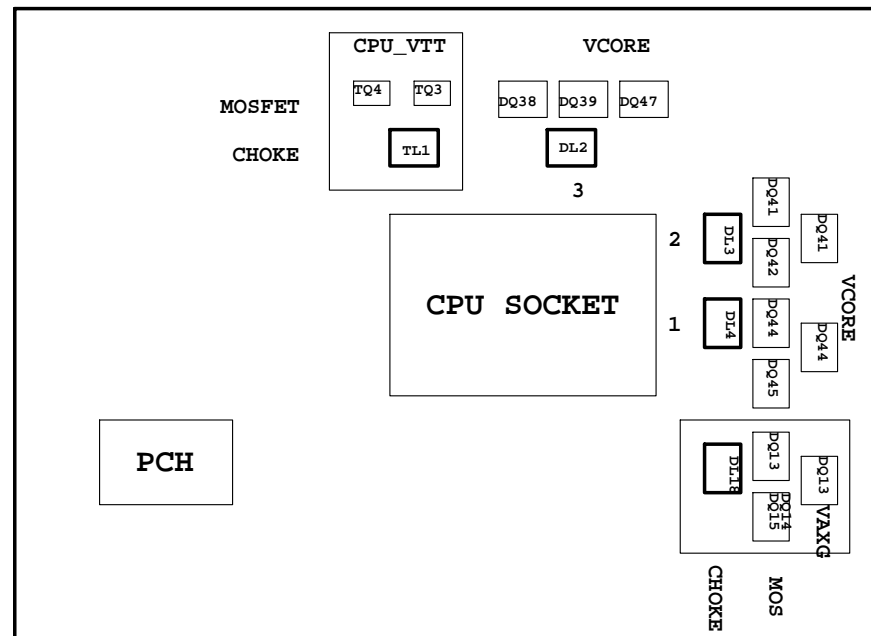
TITLE

01	COVER SHEET
02	BOM & PCB MODIFY HISTORY
03	BLOCK DIAGRAM
04	CPU_LGA1155-A
05	CPU_LGA1155-B
06	CPU_LGA1155-C
07	DDR III CHANNEL A
08	DDR III CHANNEL B
09	PCH_FDI,DMI,USB,PCIE
10	PCH_DP,CLK BUFFER
11	PCH_HOST,SATA,PCI
12	PCH_GPIO,CTRL,AUDIO
13	PCH_PWR,GND
14	PCI EXPRESS*16 SLOT
15	PCI EXPRESSX4 SLOT / PCIE X1 SLOT
16	PCI SLOT 1~2
17	I/O ITE8728
18	COM, LPT, TPM
19	Dual BIOS
20	VIA2021
21	REAR AUDIO JACK
22	ISL95836_VCORE_1
23	ISL95836_VCORE_2
24	DISCRETE POWER
25	PCH CORE / VOLTAGE CONSOLE
26	RT8120_CPU_VTT
27	VCCSA POWER

SHEET

TITLE

28	F_PANEL , F_USB
29	ATX POWER, CLOCK GEN
30	HWM,KB/MS , FAN CTRL
31	ARTHEROS AR8161/AR8151
32	mSATA
33	RT8120_DDR POWER
34	DVI
35	
36	
37	
38	
39	
40	



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Title		
Cover Sheet		
Size	Document Number	Rev
Custom	GA-Z77-DS3H	1.0
Date:	Wednesday, March 28, 2012	Sheet 1 of 35

Component value change history

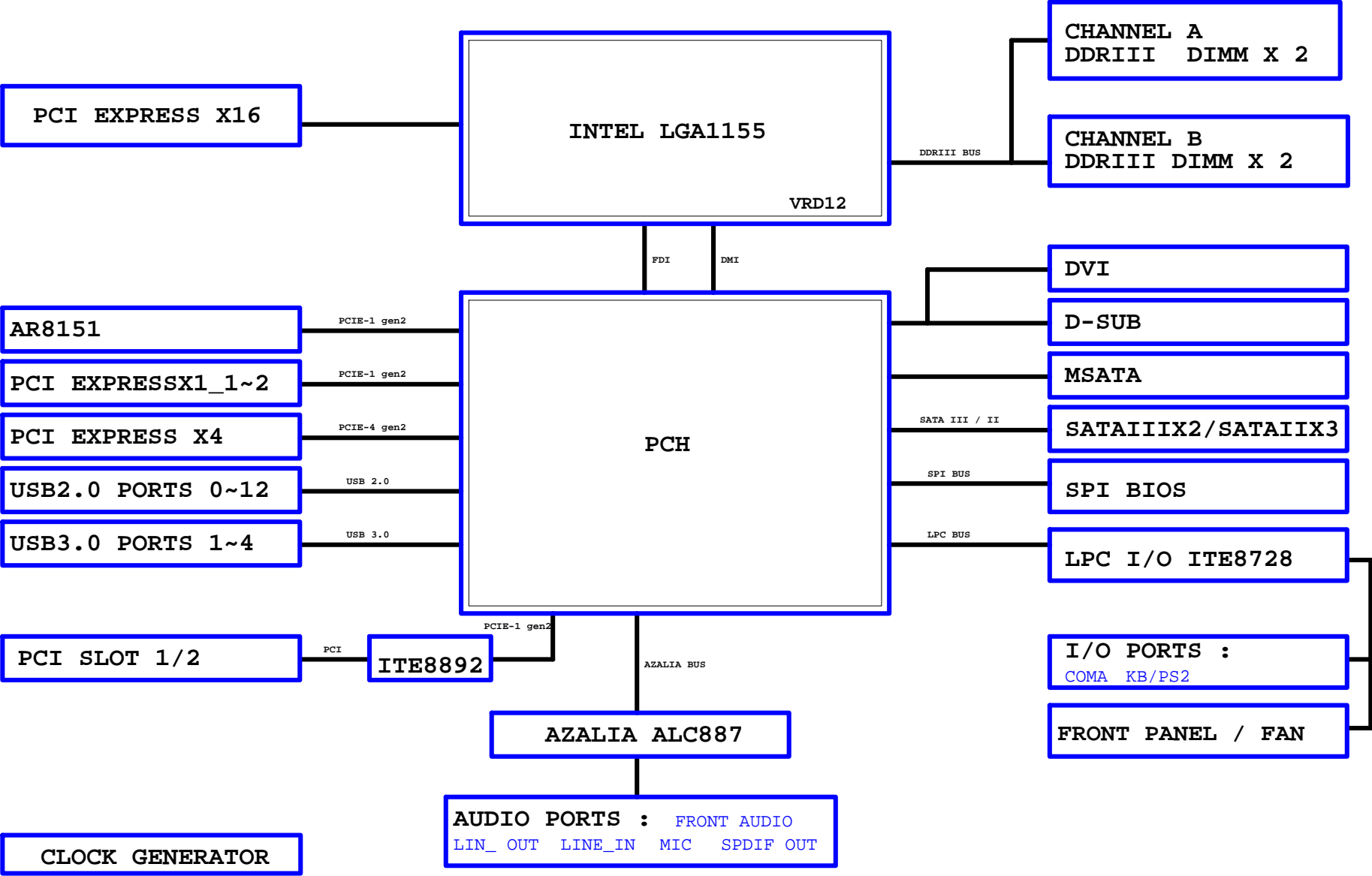
Circuit or PCB layout change

Gigabyte Technology

Title	BOM & PCB MODIFY HISTORY
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Size Custom	Document Number GA-Z77-DS3H	Rev 1.0
Date:	Wednesday, March 28, 2012	Sheet 2 of 35

BLOCK DIAGRAM



LGA1155A

MAAA0	AV27	SA_MA[0]	SA_DS[0]	AK3	DQSA0
MAAA1	AY24	SA_MA[1]	SA_DS[0]	AK2	-DQSA0
MAAA2	AW24	SA_MA[2]			
MAAA3	AW23	SA_MA[3]			
MAAA4	AV23	SA_MA[4]	SA_DS[0]	AJ3	MDA0
MAAA5	AT24	SA_MA[5]	SA_DS[1]	AJ4	MDA1
MAAA6	AT23	SA_MA[6]	SA_DS[2]	AL3	MDA2
MAAA7	AU22	SA_MA[7]	SA_DS[3]	AL4	MDA3
MAAA8	AV22	SA_MA[8]	AJ2	MDA4	
MAAA9	AT22	SA_MA[9]	SA_DS[4]	AJ1	MDA5
MAAA10	AV28	SA_MA[10]	SA_DS[5]	AL2	MDA6
MAAA11	AU21	SA_MA[11]	SA_DS[6]	AL1	MDA7
MAAA12	AT21	SA_MA[12]			
MAAA13	AW32	SA_MA[13]	SA_DS[11]	AP3	DQSA1
MAAA14	AU20	SA_MA[14]	SA_DS[11]	AP2	-DQSA1
MAAA15	AT20	SA_MA[15]			
[7] -SWEA	AW29	SA_WE#	SA_DS[8]	AN1	MDA8
[7] -SCASA	AV30	SA_CAS#	SA_DS[9]	AN4	MDA9
[7] -SRASA	AU28	SA_RAS#	SA_DS[10]	AR3	MDA10
			SA_DS[11]	AR4	MDA12
[7] SBAA0	AY29	SA_BS[0]	SA_DS[12]	AN2	MDA11
[7] SBAA1	AW28	SA_BS[1]	SA_DS[13]	AN3	MDA13
[7] SBAA2	AV20	SA_BS[2]	SA_DS[14]	AR2	MDA14
			SA_DS[15]	AR1	MDA15
[7] -CSA0	AY29	SA_CS#	SA_DS[2]	AW4	DQSA2
[7] -CSA1	AV32	SA_CS#	SA_DS[2]	AW4	-DQSA2
[7] -CSA2	AW30	SA_CS#	SA_DS[2]		
[7] -CSA3	AU33	SA_CS#			
[7] CKEA0	AV19	SA_CKE[0]	SA_DS[16]	AV2	MDA16
[7] CKEA1	AT19	SA_CKE[1]	SA_DS[17]	AW3	MDA17
[7] CKEA2	AU18	SA_CKE[2]	SA_DS[18]	AV5	MDA18
[7] CKEA3	AV18	SA_CKE[3]	SA_DS[19]	AW5	MDA19
			SA_DS[20]	AU2	MDA20
MODT_A0	AV31	SA_ODT[0]	SA_DS[21]	AU3	MDA21
MODT_A1	AU32	SA_ODT[1]	SA_DS[22]	AY5	MDA22
MODT_A2	AU30	SA_ODT[2]	SA_DS[23]	AY5	MDA23
MODT_A3	AW33	SA_ODT[3]			
[7] DCLKA0	AY25	SA_CK[0]	SA_DS[3]	AV8	DQSA3
[7] -DCLKA0	AW25	SA_CK#	SA_DS[3]	AW8	-DQSA3
[7] DCLKA1	AU24	SA_CK[1]			
[7] -DCLKA1	AU25	SA_CK#	SA_DS[24]	AY7	MDA24
[7] DCLKA2	AW27	SA_CK[2]	SA_DS[25]	AU7	MDA25
[7] -DCLKA2	AY27	SA_CK#	SA_DS[26]	AV9	MDA26
[7] DCLKA3	AW26	SA_CK[3]	SA_DS[27]	AU9	MDA27
[7] -DCLKA3	AY26	SA_CK#	SA_DS[28]	AV7	MDA28
			SA_DS[29]	AW7	MDA29
			SA_DS[30]	AW9	MDA30
			SA_DS[31]	AY9	MDA31
[7,8] -DDR3_RST	TR1	SM_DRAMRST#	SA_DS[4]	AV37	DQSA4
	TBC9		SA_DS[4]	AV36	-DQSA4
	0.1u4/X7R/16V/K/X				
			SA_DS[32]	AU35	MDA32
			SA_DS[33]	AW37	MDA33
			SA_DS[34]	AU39	MDA34
			SA_DS[35]	AU36	MDA35
			SA_DS[36]	AW35	MDA36
			SA_DS[37]	AY36	MDA37
			SA_DS[38]	AU38	MDA38
			SA_DS[39]	AU37	MDA39
			SA_DS[40]	AP38	DQSA5
			SA_DS[41]	AP39	-DQSA5
			SA_DS[42]		
			SA_DS[43]	AR40	MDA40
			SA_DS[44]	AR37	MDA41
			SA_DS[45]	AN38	MDA42
			SA_DS[46]	AN37	MDA43
			SA_DS[47]	AR39	MDA44
			SA_DS[48]	AR38	MDA45
			SA_DS[49]	AN39	MDA46
			SA_DS[50]	AN40	MDA47
			SA_DS[51]	AK38	DQSA6
			SA_DS[52]	AK39	-DQSA6
			SA_DS[53]		
			SA_DS[54]	AL40	MDA48
			SA_DS[55]	AL37	MDA49
			SA_DS[56]	AJ38	MDA50
			SA_DS[57]	AJ37	MDA51
			SA_DS[58]	AL39	MDA52
			SA_DS[59]	AL38	MDA53
			SA_DS[60]	AJ39	MDA54
			SA_DS[61]	AJ40	MDA55
			SA_DS[62]	AF38	DQSA7
			SA_DS[63]	AF39	-DQSA7
			SA_DS[64]	AG40	MDA56
			SA_DS[65]	AG37	MDA57
			SA_DS[66]	AE38	MDA58
			SA_DS[67]	AE37	MDA59
			SA_DS[68]	AG39	MDA60
			SA_DS[69]	AG38	MDA61
			SA_DS[70]	AE39	MDA62
			SA_DS[71]	AE40	MDA63

DDR_0

1 OF 10

CPU-SK/1155/S/15

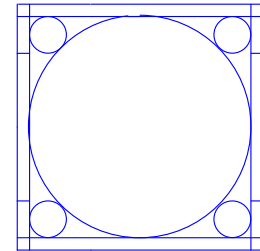
LGA1155B

MAAB0	AK24	SB_MA[0]	SB_DS[0]	AH7	DQSB0
MAAB1	AM20	SB_MA[1]	SB_DS[0]	AH6	-DQSB0
MAAB2	AM19	SB_MA[2]			
MAAB3	AK18	SB_MA[3]			
MAAB4	AP19	SB_MA[4]	SB_DS[0]	AG7	MDB0
MAAB5	AP18	SB_MA[5]	SB_DS[1]	AG8	MDB1
MAAB6	AM18	SB_MA[6]	SB_DS[2]	AJ9	MDB2
MAAB7	AL18	SB_MA[7]	SB_DS[3]	AJ8	MDB3
MAAB8	AY17	SB_MA[8]	SB_DS[4]	AG5	MDB4
MAAB9	AN18	SB_MA[9]	SB_DS[5]	AJ6	MDB5
MAAB10	AN23	SB_MA[10]	SB_DS[6]	AJ6	MDB6
MAAB11	AU17	SB_MA[11]	SB_DS[7]	AJ7	MDB7
MAAB12	AT18	SB_MA[12]			
MAAB13	AR26	SB_MA[13]	SB_DS[11]	AM8	DQSB1
MAAB14	AY16	SB_MA[14]	SB_DS[11]	AL8	-DQSB1
MAAB15	AV16	SB_MA[15]			
[8] -SWEB	AR25	SB_WE#	SB_DS[8]	AL7	MDB8
[8] -SCASB	AK25	SB_CAS#	SB_DS[9]	AM7	MDB9
[8] -SRASB	AP24	SB_RAS#	SB_DS[10]	AM10	MDB10
			SB_DS[11]	AL10	MDB11
[8] SBAB0	AP23	SB_BS[0]	SB_DS[12]	AL6	MDB12
[8] SBAB1	AW17	SB_BS[1]	SB_DS[13]	AL9	MDB13
[8] SBAB2		SB_BS[2]	SB_DS[14]	AM9	MDB15
			SB_DS[15]		
[8] -CSB0	AN25	SB_CS#	SB_DS[2]	AR8	DQSB2
[8] -CSB1	AN26	SB_CS#	SB_DS[2]	AP8	-DQSB2
[8] -CSB2	AL26	SB_CS#			
[8] -CSB3	AT26	SB_CS#			
[8] CKEB0	AL18	SB_CKE[0]	SB_DS[16]	AP7	MDB16
[8] CKEB1	AY15	SB_CKE[1]	SB_DS[17]	AR7	MDB17
[8] CKEB2	AW15	SB_CKE[2]	SB_DS[18]	AR10	MDB18
[8] CKEB3	AV15	SB_CKE[3]	SB_DS[19]	AR10	MDB19
			SB_DS[20]	AP6	MDB20
MODT_B0	AL26	SB_ODT[0]	SB_DS[21]	AP9	MDB21
MODT_B1	AP26	SB_ODT[1]	SB_DS[22]	AR9	MDB22
MODT_B2	AM26	SB_ODT[2]	SB_DS[23]	AR9	MDB23
MODT_B3	AK26	SB_ODT[3]			
[8] DCLKB0	AL21	SB_CK[0]	SB_DS[3]	AN13	DQSB3
[8] -DCLKB0	AL22	SB_CK#	SB_DS[3]	AN12	-DQSB3
[8] DCLKB1	AL20	SB_CK[1]			
[8] -DCLKB1	AK20	SB_CK#	SB_DS[24]	AM12	MDB24
[8] DCLKB2	AL23	SB_CK[2]	SB_DS[25]	AM13	MDB25
[8] -DCLKB2	AM22	SB_CK#	SB_DS[26]	AR13	MDB26
[8] DCLKB3	AP21	SB_CK[3]	SB_DS[27]	AR13	MDB27
[8] -DCLKB3	AN21	SB_CK#	SB_DS[28]	AL12	MDB28
			SB_DS[29]	AL13	MDB29
			SB_DS[30]	AR12	MDB30
			SB_DS[31]	AP12	MDB31
			SB_DS[32]	AN29	DQSB4
			SB_DS[33]	AN28	-DQSB4
[8] VREF_DQB	AH1	FC_AH1	SB_DS[4]	AR28	MDB32
[7] VREF_DQA	AH4	FC_AH4	SB_DS[4]	AR29	MDB33
			SB_DS[34]	AL28	MDB34
			SB_DS[35]	AL29	MDB35
			SB_DS[36]	AP28	MDB36
			SB_DS[37]	AP29	MDB37
			SB_DS[38]	AM28	MDB38
			SB_DS[39]	AM29	MDB39
			SB_DS[40]	AP33	DQSB5
			SB_DS[41]	AR33	-DQSB5
			SB_DS[42]		
			SB_DS[43]	AP32	MDB40
			SB_DS[44]	AP21	MDB41
			SB_DS[45]	AP35	MDB42
			SB_DS[46]	AP34	MDB43
			SB_DS[47]	AR32	MDB44
			SB_DS[48]	AR31	MDB45
			SB_DS[49]	AR35	MDB46
			SB_DS[50]	AR34	MDB47
			SB_DS[51]	AL33	DQSB6
			SB_DS[52]	AM33	-DQSB6
			SB_DS[53]		
			SB_DS[54]	AM32	MDB48
			SB_DS[55]	AM31	MDB49
			SB_DS[56]	AL35	MDB50
			SB_DS[57]	AL32	MDB51
			SB_DS[58]	AM34	MDB52
			SB_DS[59]	AL31	MDB53
			SB_DS[60]	AM35	MDB54
			SB_DS[61]	AL34	MDB55
			SB_DS[62]	AG35	DQSB7
			SB_DS[63]	AG34	-DQSB7
			SB_DS[64]		
			SB_DS[65]	AH35	MDB56
			SB_DS[66]	AH34	MDB57
			SB_DS[67]	AE34	MDB58
			SB_DS[68]	AE35	MDB59
			SB_DS[69]	AJ35	MDB60
			SB_DS[70]	AJ34	MDB61
			SB_DS[71]	AE33	MDB62
			SB_DS[72]	AF33	MDB63

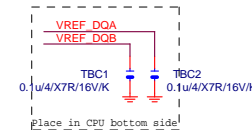
DDR_1

2 OF 10

CPU-SK/1155/S/15

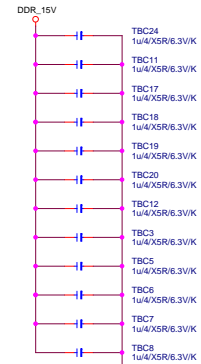
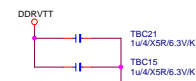
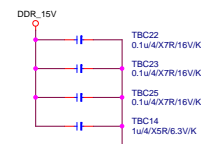
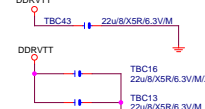
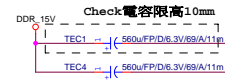
LGA1155
ILM_BP/1156/CSP

Need check the new CPU ME

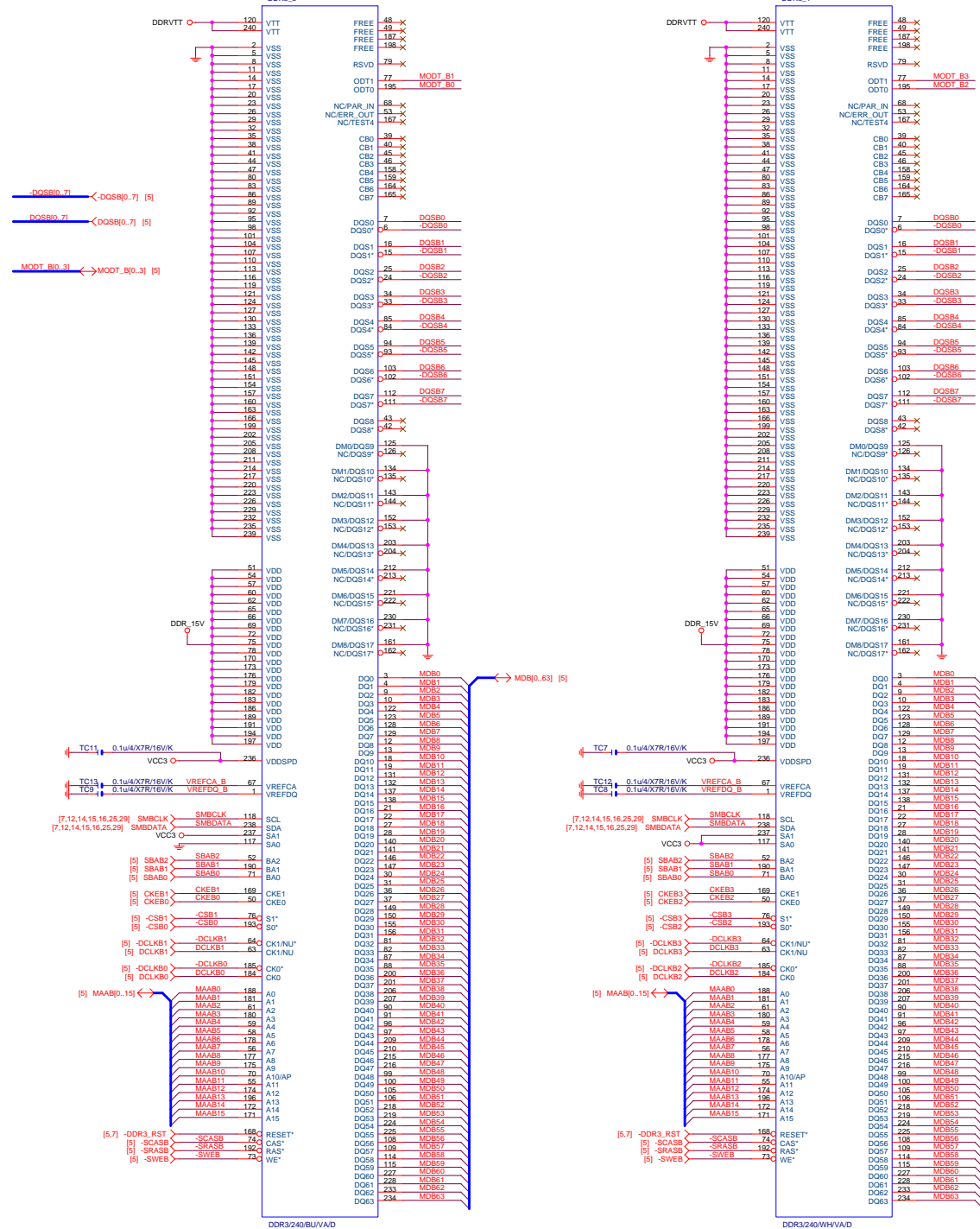


Intel CRB

Title			CPU LGA1155-B		
Size			Document Number		
Custom			GA-Z77-DS3H		
Date:			Wednesday, March 28, 2012		
Sheet			5 of 35		
Rev			1.0		



Intel CRB			
Title DDRIII CHANNEL A			
Size	Document Number		Rev
Custom	GA-Z77-DS3H		1.
Date:		Sheet 7 of 35	

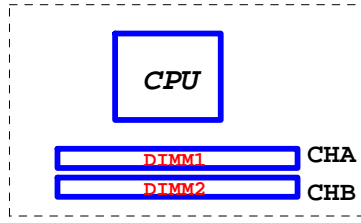


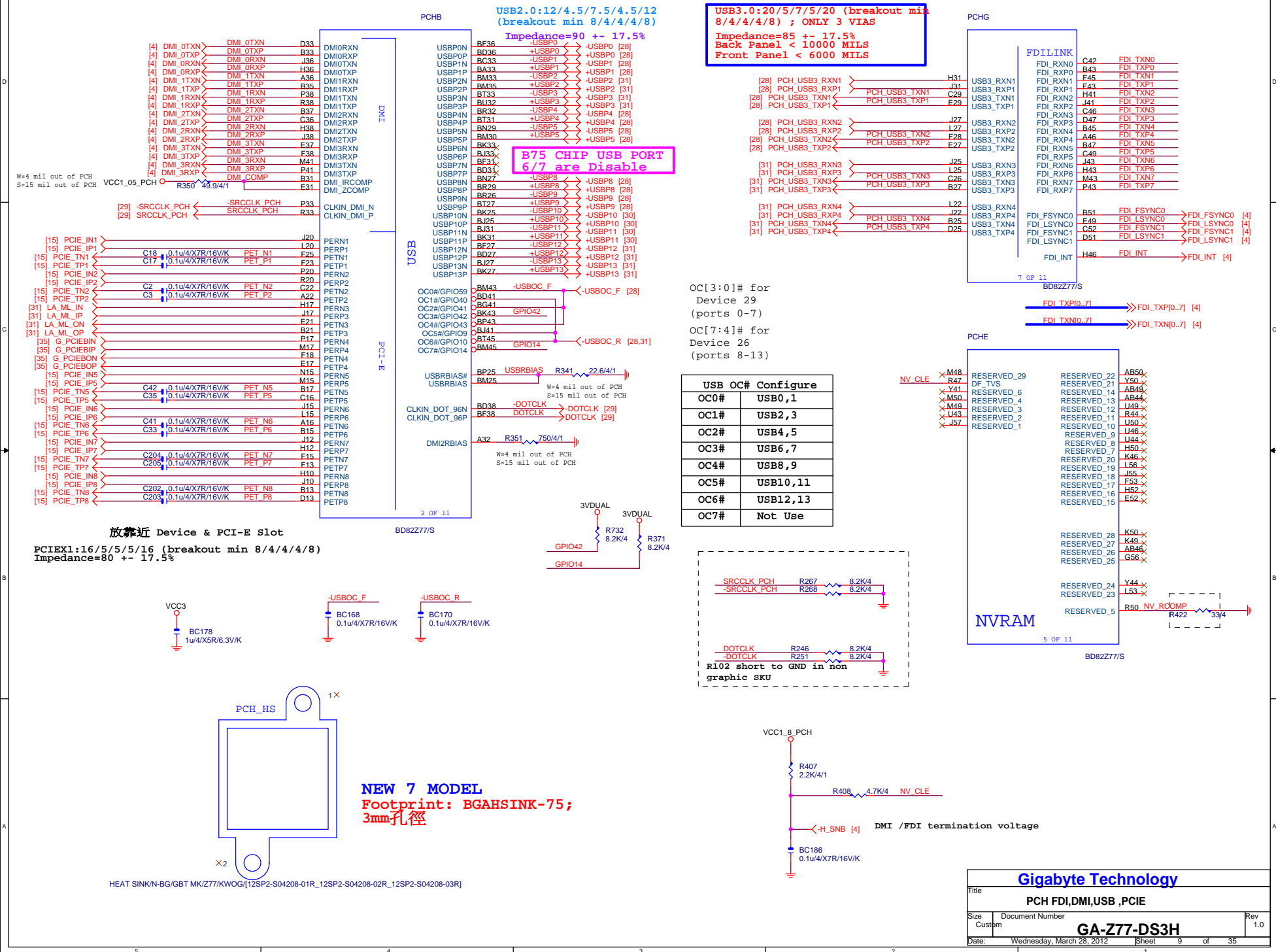
DDR3 1066,1333,1600MHZ BANDWIDTH

DDR3 1066MHZ
DDR3 clock=533MHZ
DDR3 single channel bandwidth=533x2x8Byte=8.5GB/s
DDR3 dual channel bandwidth=533x2x2x8Byte=17GB/s

DDR3 1333MHZ
DDR3 clock=667MHZ
DDR3 single channel bandwidth=10.6GB/s
DDR3 dual channel bandwidth=21GB/s

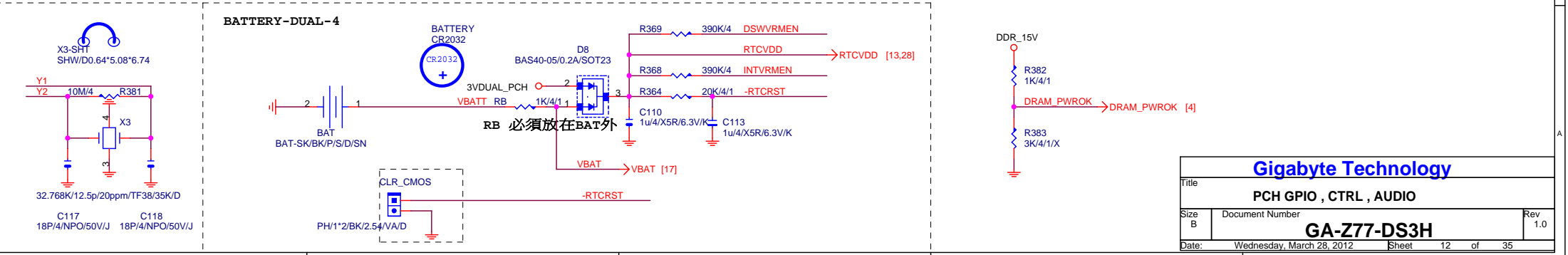
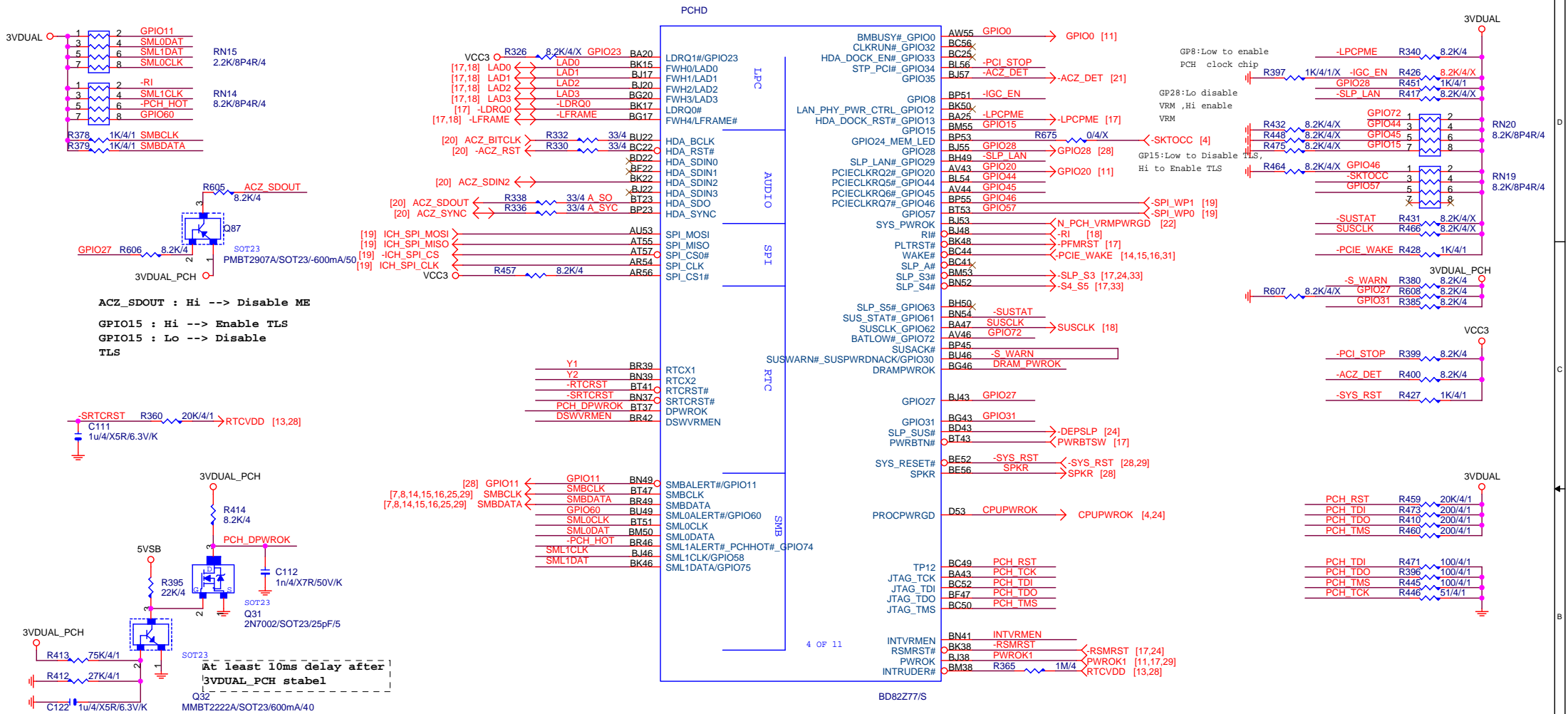
DDR3 1600MHZ
DDR3 clock=800MHZ
DDR3 single channel bandwidth=12.8GB/s
DDR3 dual channel bandwidth=25.6GB/s

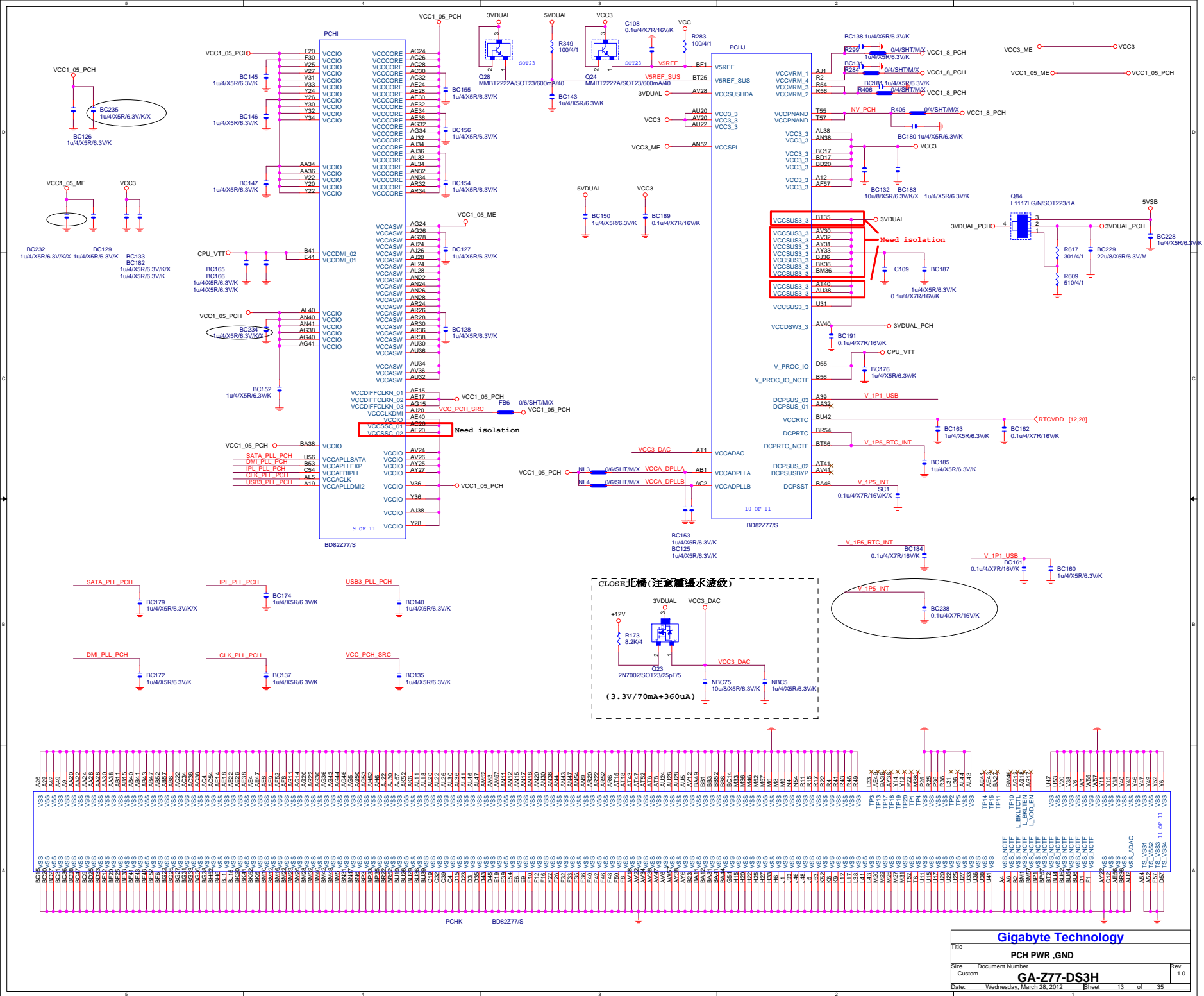




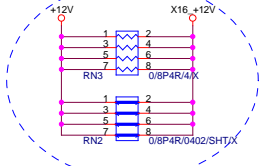
SATA2 : 15/7.5/4.5/7.5/15 (breakout min 8/4/4/4/8)
Impedance=90 +- 17.5% PCHC







+12 protect short-wire test



PCIE16:16/5/5/5/16

EXP_RXP0_15I >>> EXP_RXP0_15I [4]
EXP_RXN0_15I >>> EXP_RXN0_15I [4]
EXP_TXP0_15I >>> EXP_TXP0_15I [4]
EXP_TXN0_15I >>> EXP_TXN0_15I [4]

EXP_TXP0	C43	0.22u/4/X5R6.3V/K/EXP_TXP0C
EXP_TXN0	C36	0.22u/4/X5R6.3V/K/EXP_TXN0C
EXP_TXP1	C47	0.22u/4/X5R6.3V/K/EXP_TXP1C
EXP_TXN1	C49	0.22u/4/X5R6.3V/K/EXP_TXN1C
EXP_TXP2	C52	0.22u/4/X5R6.3V/K/EXP_TXP2C
EXP_TXN2	C54	0.22u/4/X5R6.3V/K/EXP_TXN2C
EXP_TXP3	C57	0.22u/4/X5R6.3V/K/EXP_TXP3C
EXP_TXN3	C59	0.22u/4/X5R6.3V/K/EXP_TXN3C
EXP_TXP4	C62	0.22u/4/X5R6.3V/K/EXP_TXP4C
EXP_TXN4	C64	0.22u/4/X5R6.3V/K/EXP_TXN4C
EXP_TXP5	C65	0.22u/4/X5R6.3V/K/EXP_TXP5C
EXP_TXN5	C67	0.22u/4/X5R6.3V/K/EXP_TXN5C
EXP_TXP6	C69	0.22u/4/X5R6.3V/K/EXP_TXP6C
EXP_TXN6	C71	0.22u/4/X5R6.3V/K/EXP_TXN6C
EXP_TXP7	C76	0.22u/4/X5R6.3V/K/EXP_TXP7C
EXP_TXN7	C75	0.22u/4/X5R6.3V/K/EXP_TXN7C
EXP_TXP8	C79	0.22u/4/X5R6.3V/K/EXP_TXP8C
EXP_TXN8	C80	0.22u/4/X5R6.3V/K/EXP_TXN8C
EXP_TXP9	C81	0.22u/4/X5R6.3V/K/EXP_TXP9C
EXP_TXN9	C82	0.22u/4/X5R6.3V/K/EXP_TXN9C
EXP_TXP10	C86	0.22u/4/X5R6.3V/K/EXP_TXP10C
EXP_TXN10	C87	0.22u/4/X5R6.3V/K/EXP_TXN10C
EXP_TXP11	C90	0.22u/4/X5R6.3V/K/EXP_TXP11C
EXP_TXN11	C91	0.22u/4/X5R6.3V/K/EXP_TXN11C
EXP_TXP12	C92	0.22u/4/X5R6.3V/K/EXP_TXP12C
EXP_TXN12	C93	0.22u/4/X5R6.3V/K/EXP_TXN12C
EXP_TXP13	C95	0.22u/4/X5R6.3V/K/EXP_TXP13C
EXP_TXN13	C96	0.22u/4/X5R6.3V/K/EXP_TXN13C
EXP_TXP14	C97	0.22u/4/X5R6.3V/K/EXP_TXP14C
EXP_TXN14	C98	0.22u/4/X5R6.3V/K/EXP_TXN14C
EXP_TXP15	C99	0.22u/4/X5R6.3V/K/EXP_TXP15C
EXP_TXN15	C100	0.22u/4/X5R6.3V/K/EXP_TXN15C

PCI-E REV:1.1--> 2.5GHZ

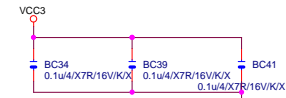
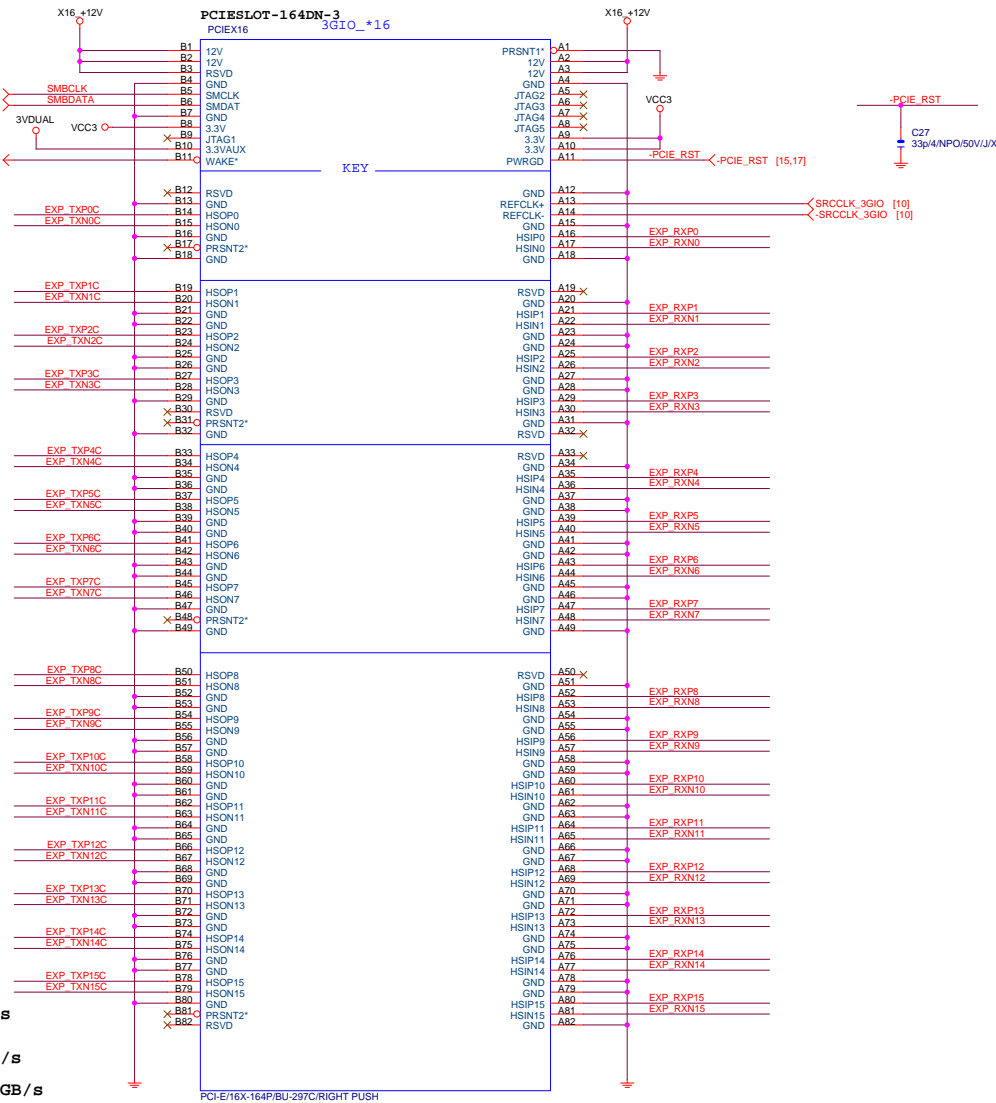
PCE-E X1(單向) BANDWITH=2.5GHZ*(8b/10b)=2Gb/s=250MB/s

PCE-E X1(雙向) BANDWITH=2.5GHZ*(8b/10b)X2=4Gb/s=500MB/s

PCE-E X16(單向) BANDWITH=2.5GHZ*(8b/10b)X16=32Gb/s=4GB/s

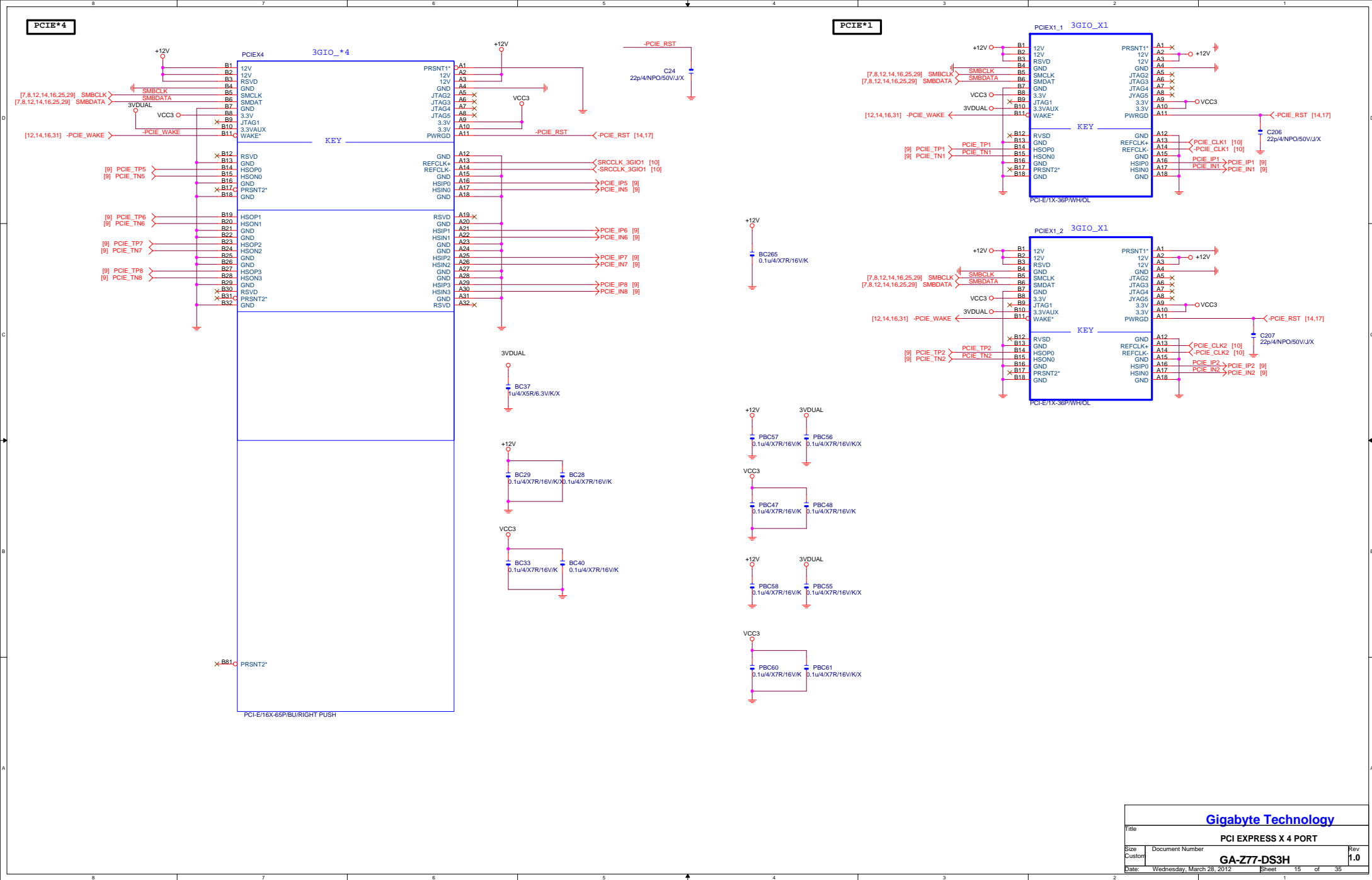
PCE-E X16(雙向) BANDWITH=2.5GHZ*(8b/10b)X16X2=64Gb/s=8GB/s

PCI-E REV:2.0--> 5GHZ



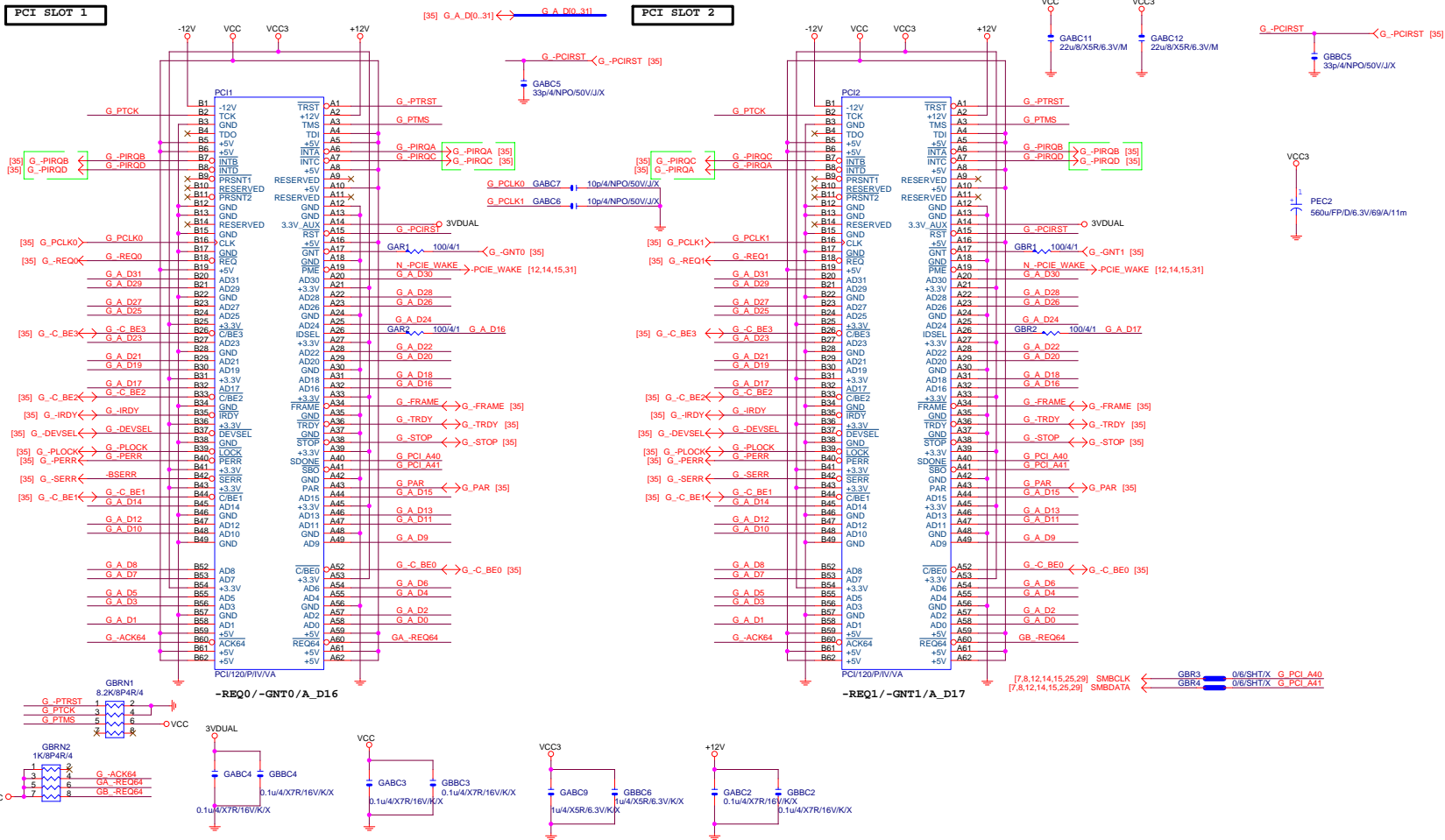
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Title			PCI EXPRESS * 16
Size	Document Number	GA-Z77-DS3H	
Custom		Rev	1.0
Date:	Wednesday, March 28, 2012	Sheet	14 of 35

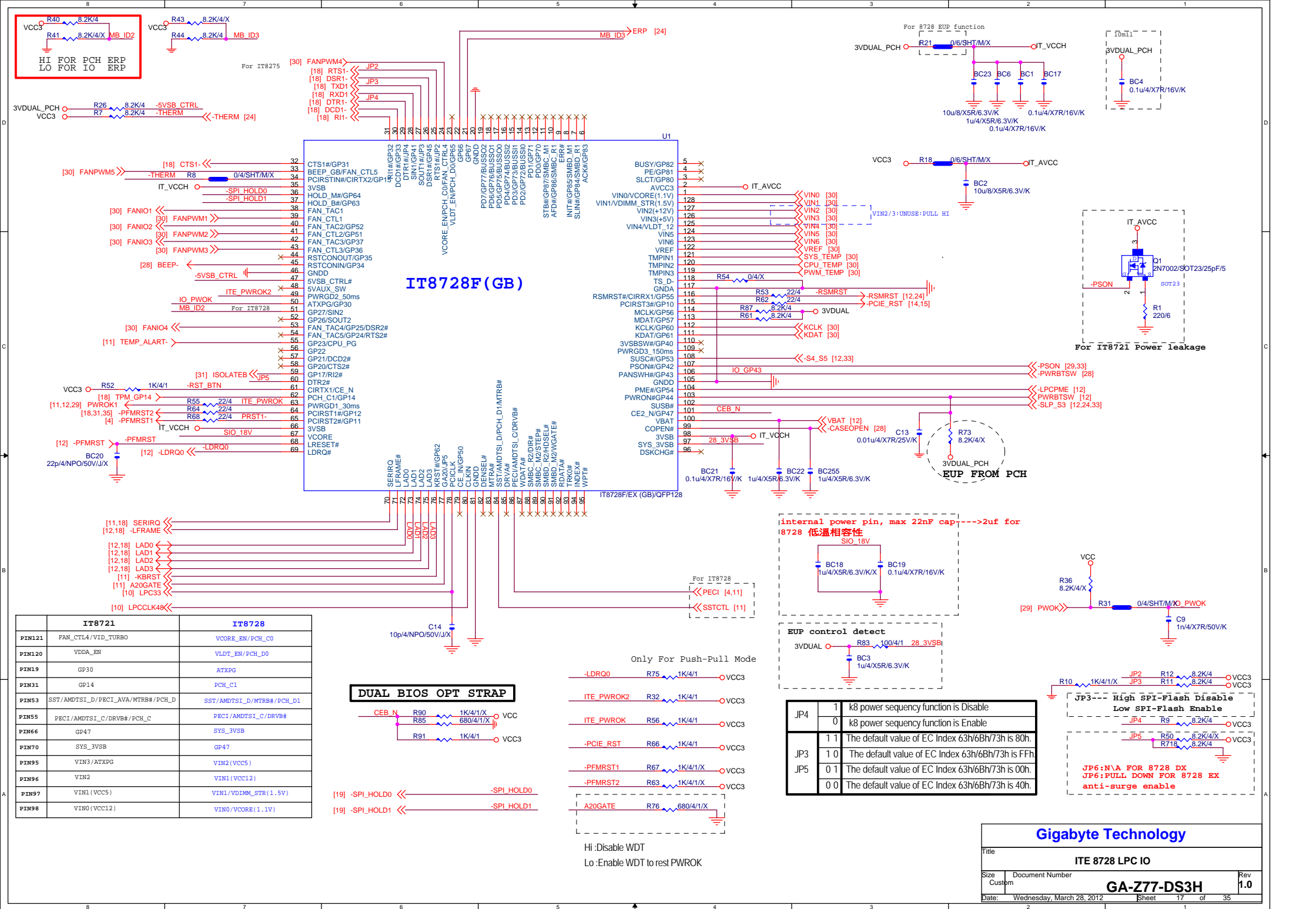


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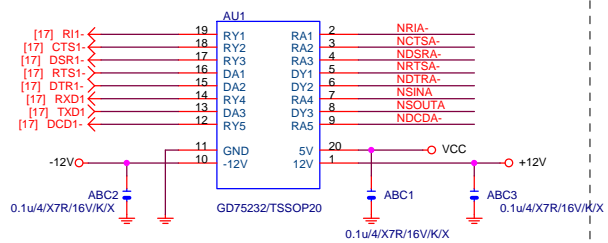
PCI SLOT 2



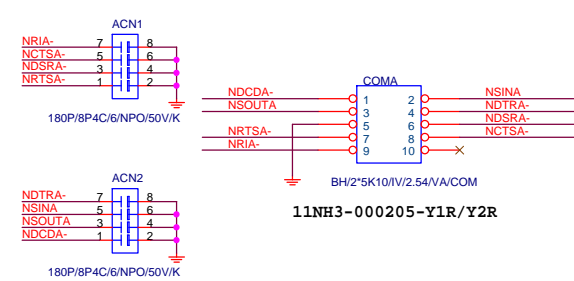
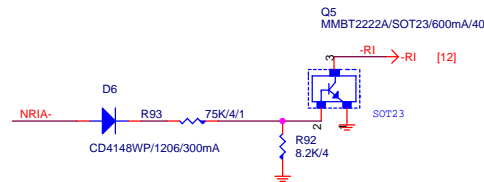
GIGABYTE™			
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Size	Document Number	Rev	
Custom	GA-Z77-DS3H	1.0	
Date:	Wednesday, March 28, 2012	Sheet	16 of 35



COMA

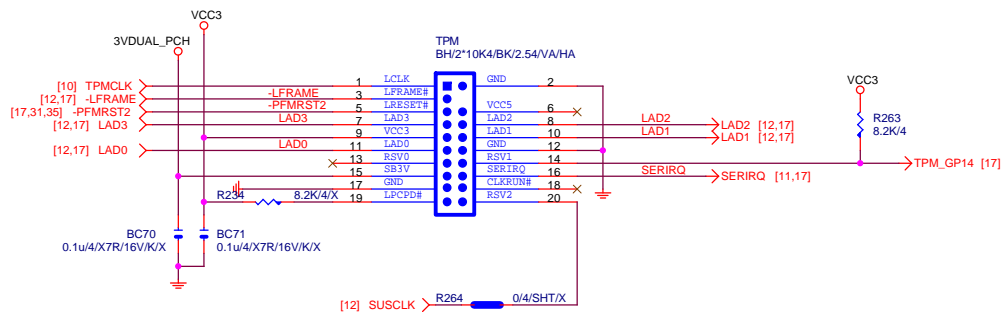


COM RI



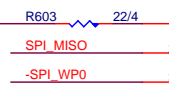
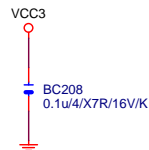
LPT PORT

TPM

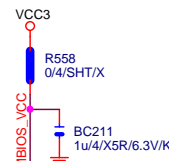
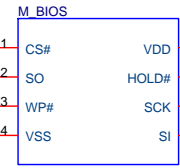


Gigabyte Technology

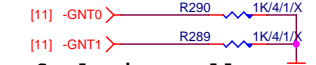
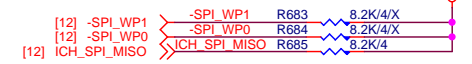
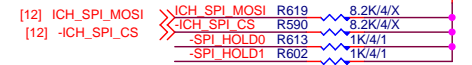
Title			
COM & Print			
Size	Document Number	Rev	1.0
Custom	GA-Z77-DS3H		
Date:	Wednesday, March 28, 2012	Sheet	18 of 35



64M/SPI/SCB/200mil/S/[10HP4-112564-20R]



MOSI For DMI RX Termination Voltage



Default int pull up



BOOT DEVICE	GNT0	GNT1
LPC	0	0
PCI	0	1
NAND	1	0
SPI	1	1

1 means floating
0 means PD 1K

Gigabyte Technology

BIOS

Size Custom

Document Number

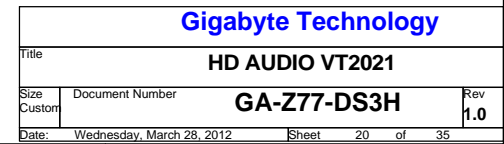
GA-Z77-DS3H

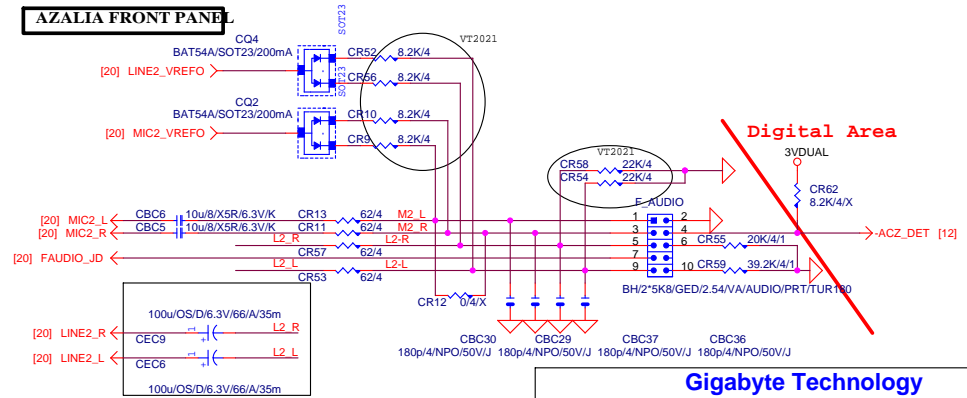
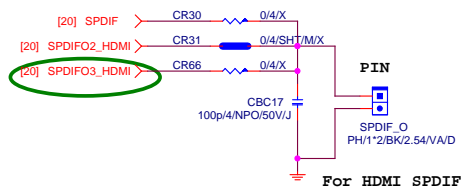
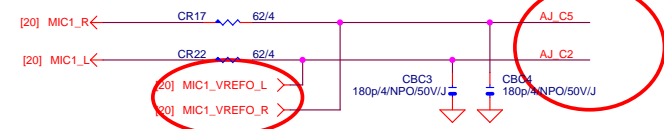
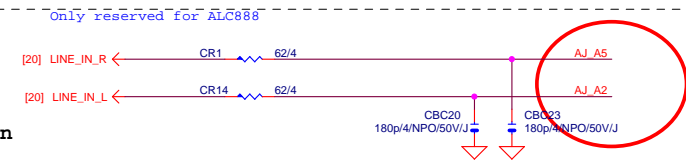
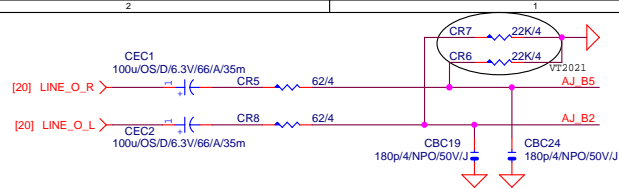
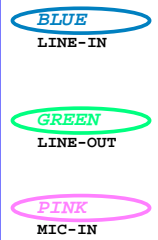
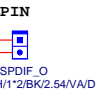
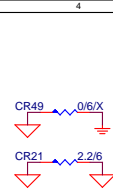
Rev
1.0

Date: Wednesday, March 28, 2012Sheet 19 of 35

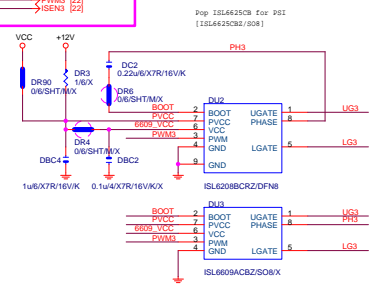
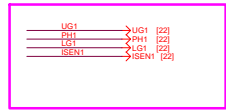
CR26: 20K/4/0.1% @ALC889A
CR26: 20K/4/1% @others

CR34 20K/4/1 VT1708S :5.1K + 100PF
CBC40 100p/4/NF0/50V/J/X





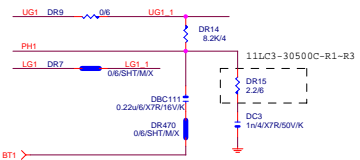
Gigabyte Technology			
Title			
AUDIO JACK			
Size Custom	Document Number	GA-Z77-DS3H	Rev 1.0
Date:	Wednesday, March 28, 2012	Sheet 21 of 35	



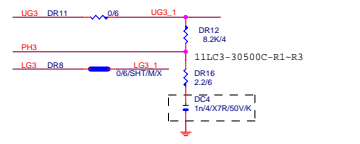
6609 colay with 6208



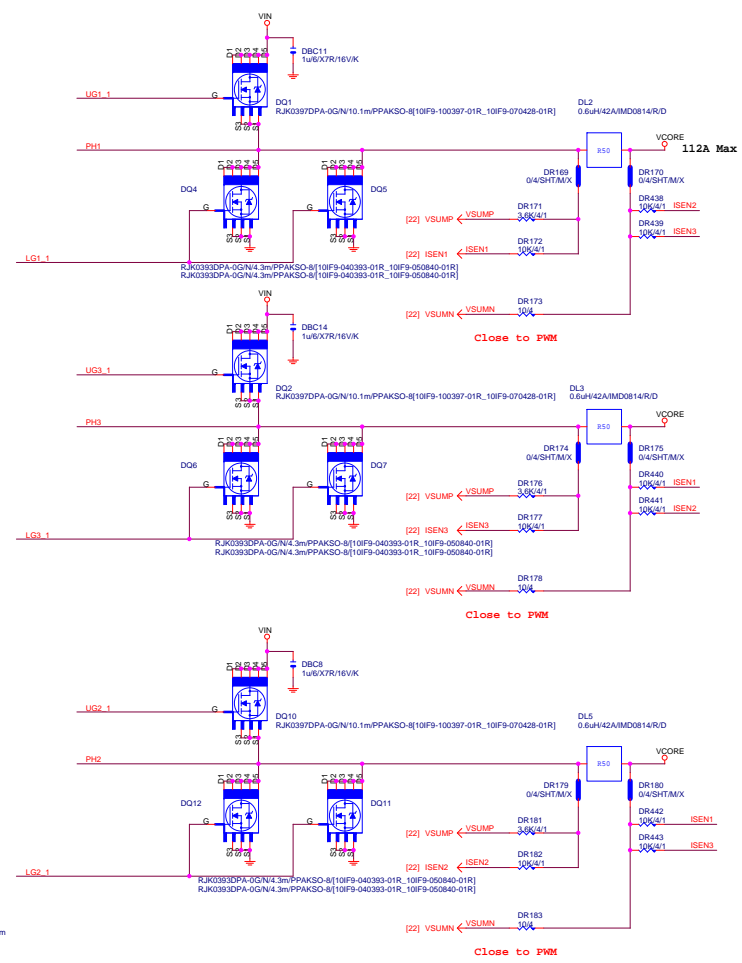
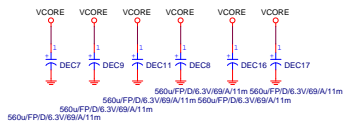
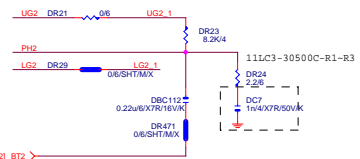
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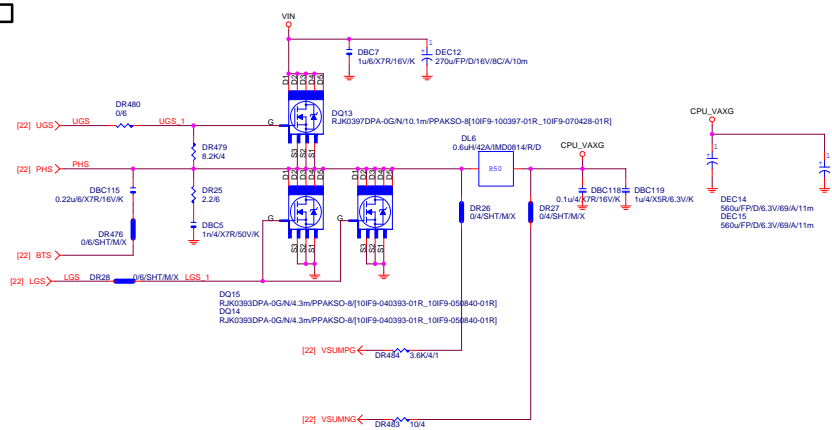
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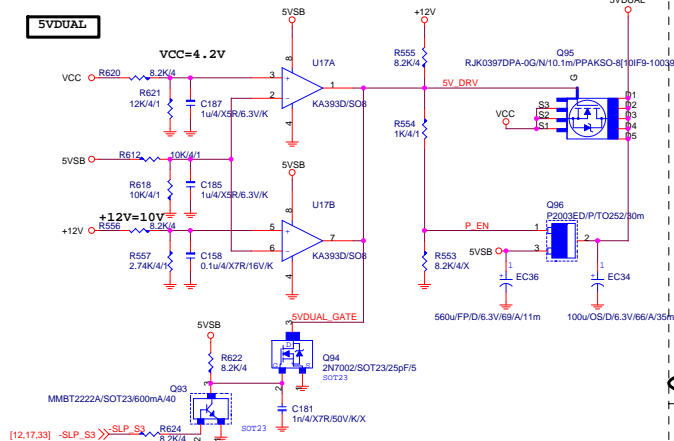
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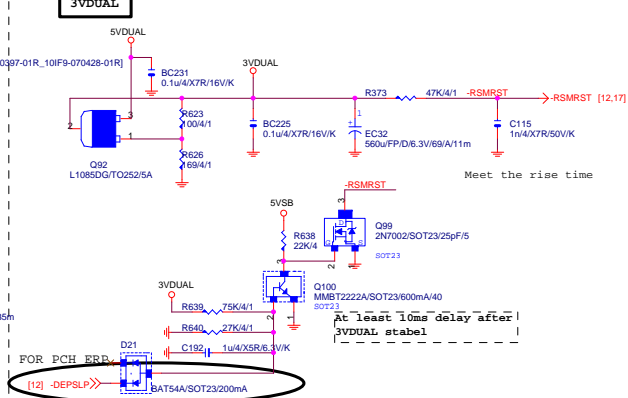
VAXG



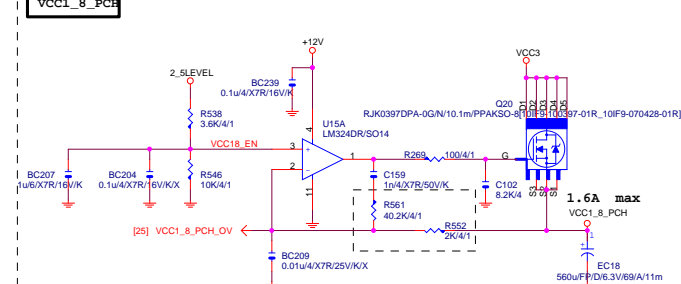
5VDUAL



3VDUAL

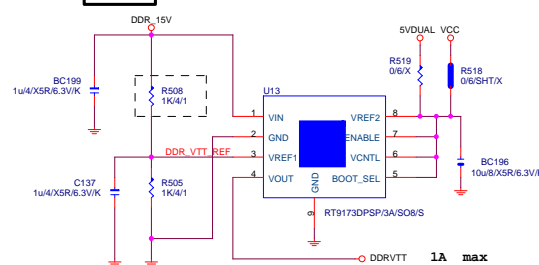


VCC1_8_PCH

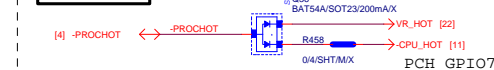


I/O ErP Control

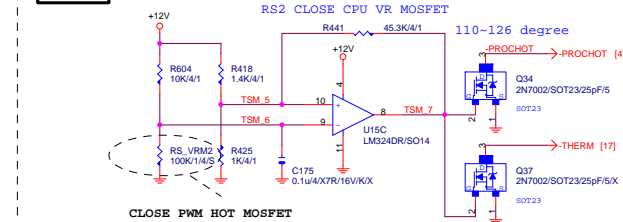
DDRVTT



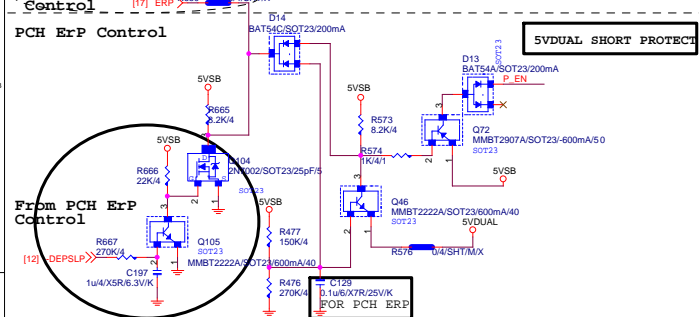
OTP PROTECT



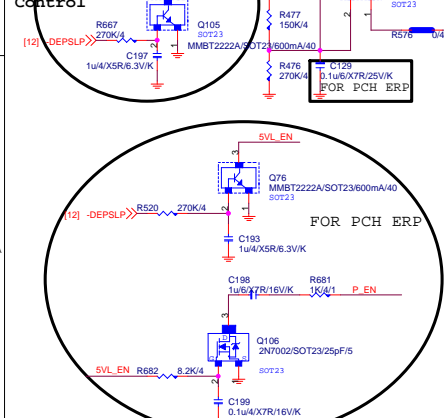
-PROHOT



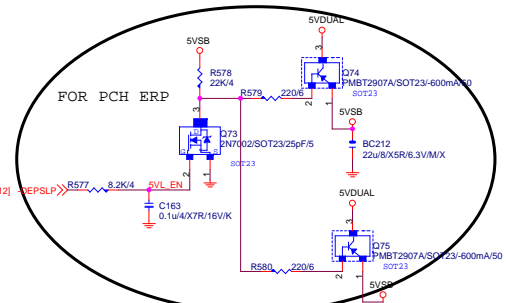
From I/O ErP Control



From PCH ErP Control



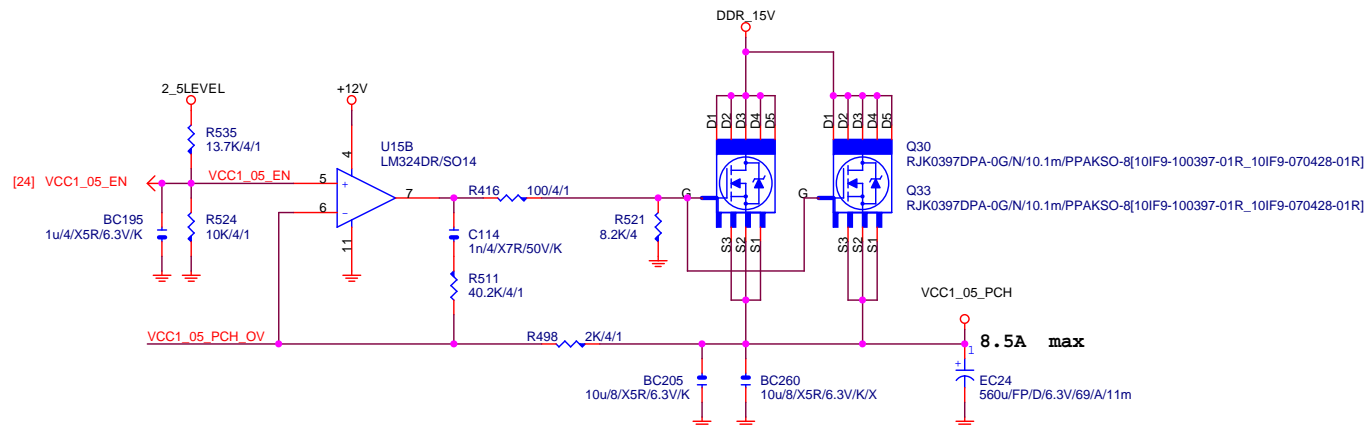
FOR PCH ERP



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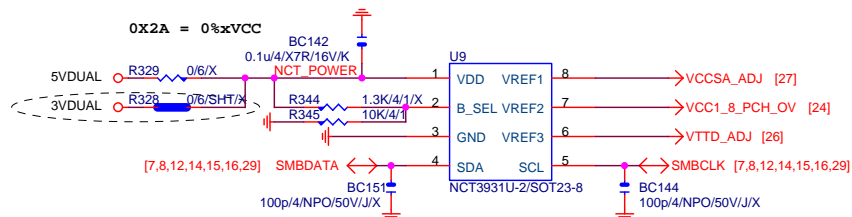
File	DISCRETE POWER	Rev
Size	Document Number	GA-Z77-DS3H
C		1.0
Date	Wednesday, March 28, 2012	Sheet 24 of 35

VCC1_05_PCH

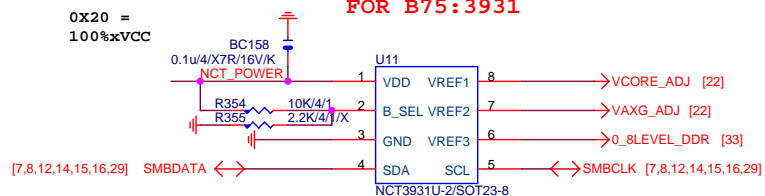


Voltage console

ADDRESS	0X2A	0X20	0X22	0X26
R1(K)	OPEN	10	1.3	3
R2(K)	10	OPEN	3.9	2.2
%VCC	0	100	75	42



FOR B75:3931



up6262	0X2A	0X20
VREF1	VCC1_05_PCH	VCORE
VREF2	VCC1_8_PCH	VCCSA
VREF3	CPU_VTT	DDR

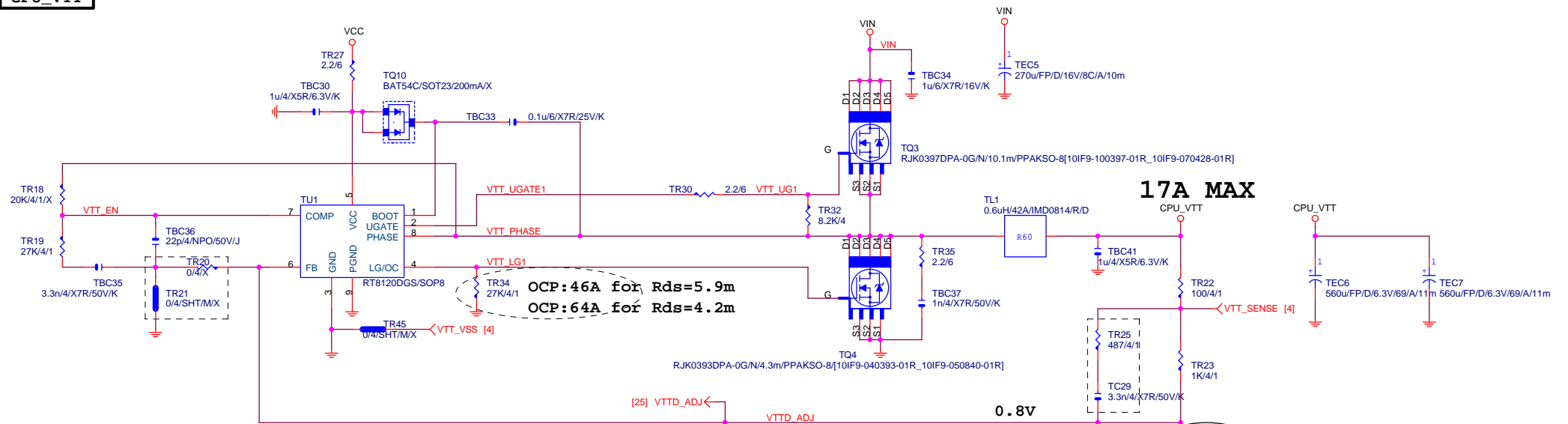
Gigabyte Technology

Title	PCH CORE / VOLTAGE CONSOLE
-------	----------------------------

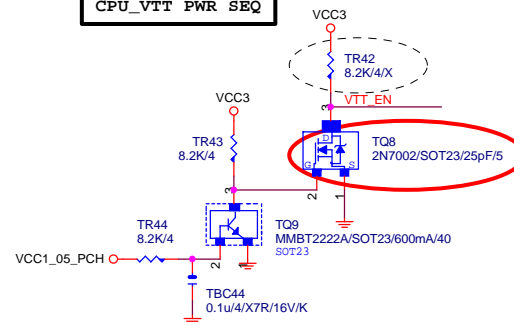
Size B	Document Number GA-Z77-DS3H	Rev 1.0
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Date: Wednesday, March 28, 2012 Sheet 25 of 35

CPU_VTT


$$\text{OCP:46A} = \text{Roset} * \text{Iocset} / \text{Rds(on)} \\ = 27\text{K} * 10\mu\text{A} / 5.9\text{m}$$

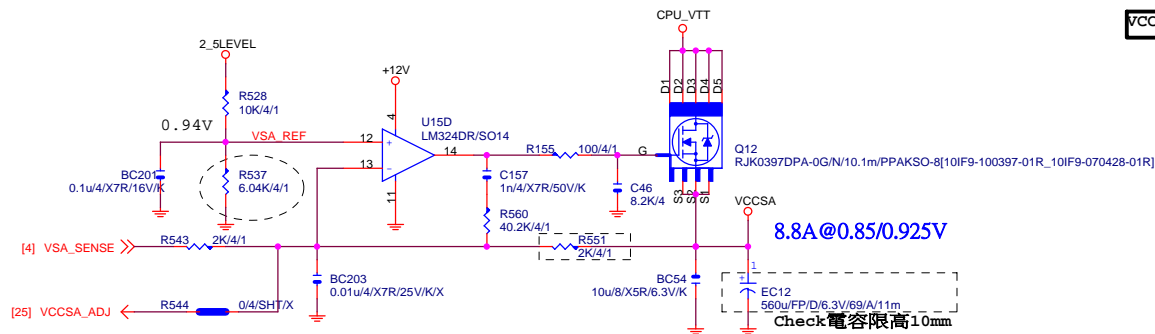
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0	0	2
0	0	3
0	0	4
0	0	5
0	0	6
0	0	7
0	0	8
0	0	9
0	0	10
0	0	11
0	0	12
0	0	13
0	0	14
0	0	15
0	0	16
0	0	17
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0	0	127
0	0	128
0	0	129
0	0	130
0	0	131
0	0	132
0	0	133
0	0	134
0	0	135
0	0	136
0	0	137
0	0	1



	VTT_SEL
HI	1.05V
LO	1.0V

According intel
CDI/IBP#476733, 固定1.05V

VCC_SA

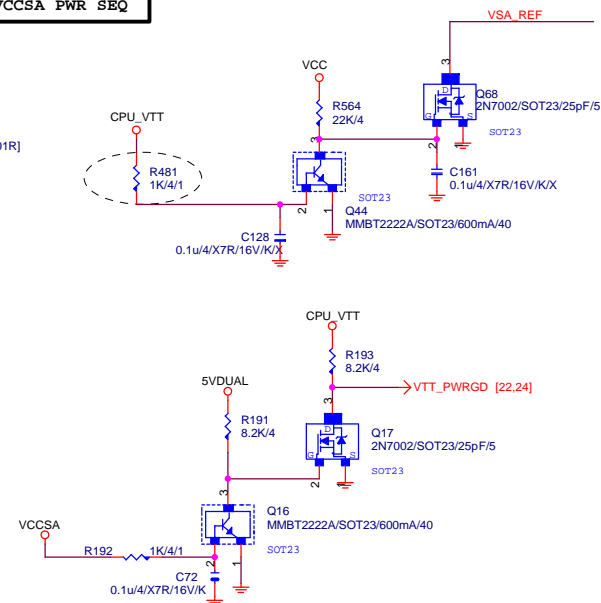


PDG 1.01

	VSA_SEL
HI	0.85V
LO	0.925V

According intel
CDI/IBP#476733, 固定0.925V

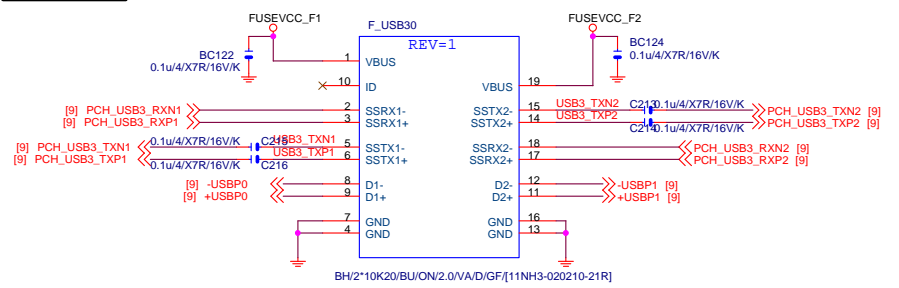
VCCSA PWR SEQ



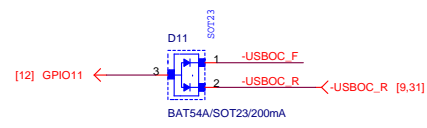
Gigabyte Technology

Title		
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Size	Document Number	Rev
Custom	GA-Z77-DS3H	1.0
Date:	Wednesday, March 28, 2012	Sheet 27 of 35

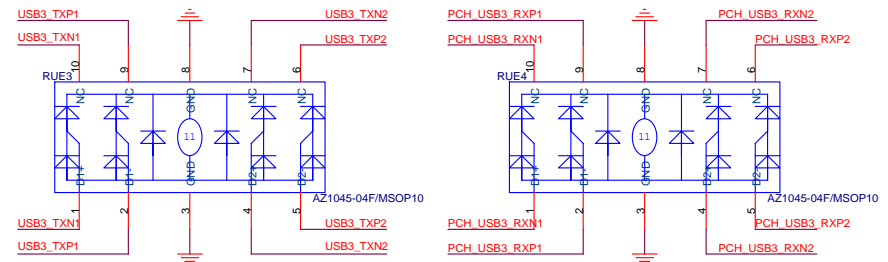
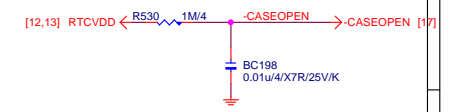
FRONT USB1



F_USB POWER PROTECT

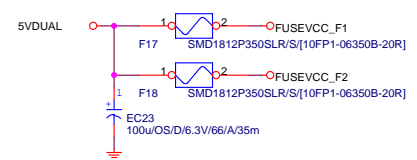
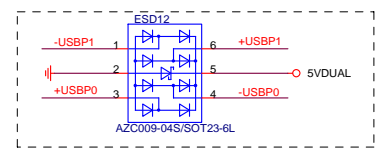


CASE OPEN

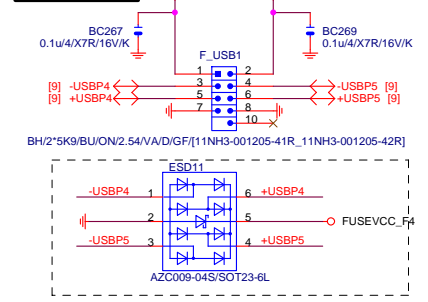


ESD Close to connector

ESD Close to connector

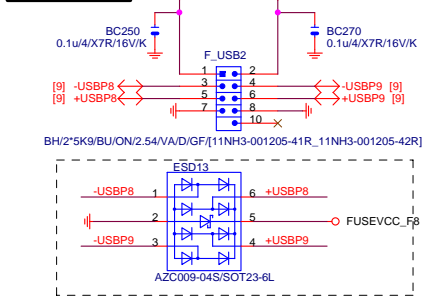


FRONT USB1

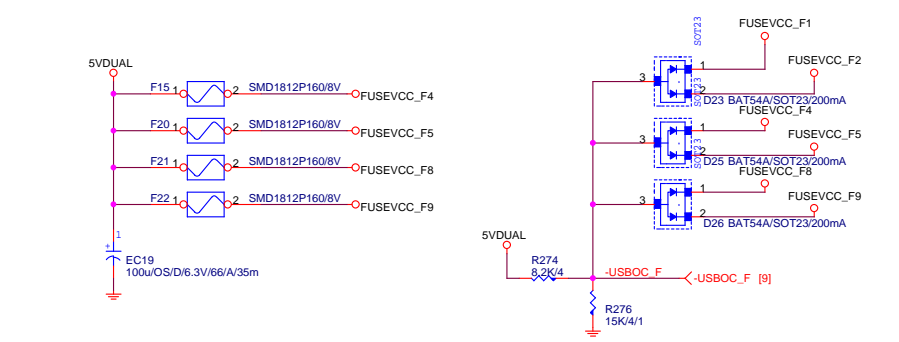


ESD Close to connector

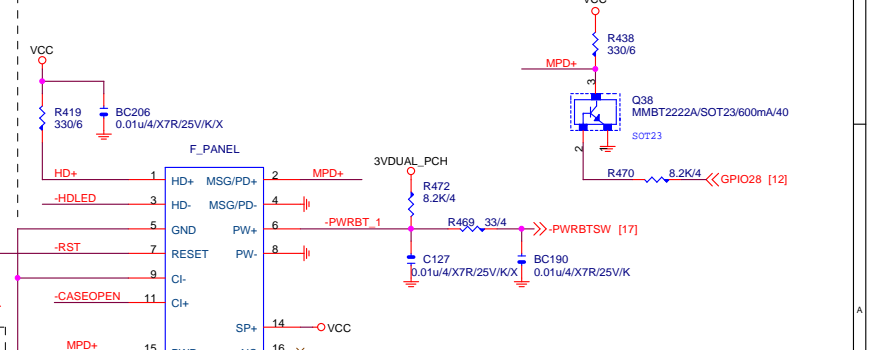
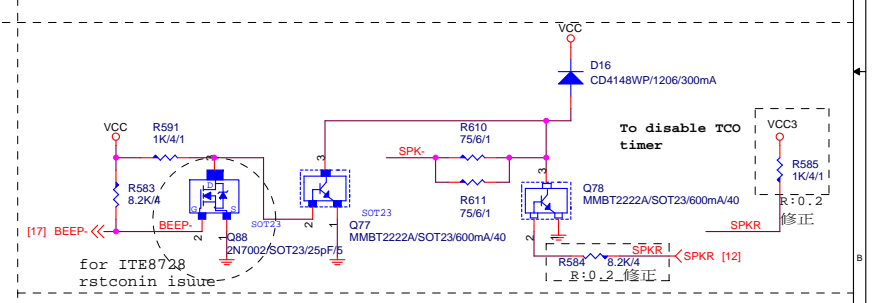
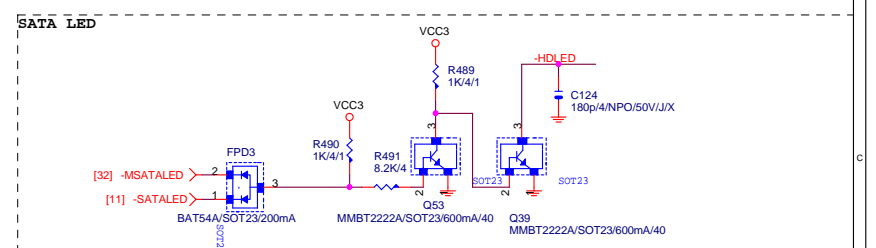
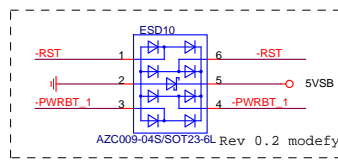
FRONT USB2



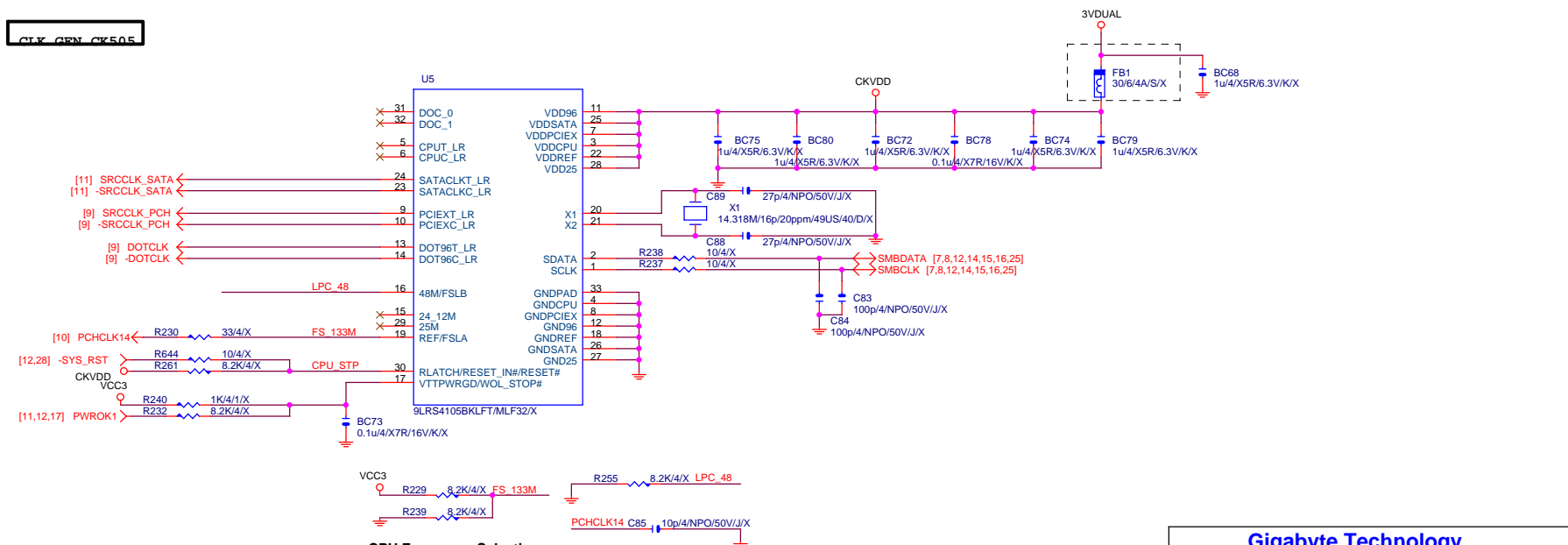
ESD Close to connector



INTEL FRONT PANEL



Gigabyte Technology			
Title			
FF,P_USB,USB PWR,FDD,BZ			
Size	Document Number	GA-Z77-DS3H	
Custom			Rev 1.0
Date:	Wednesday, March 28, 2012	Sheet	28 of 35



Gigabyte Technology			
Title			
ATX POWER CONNECTOR			
Size Custom	Document Number	GA-Z77-DS3H	Rev 1.0
Date:	Wednesday, March 28, 2012	Sheet	29 of 35

TEMP H/W MONITOR

Rev 0.2 modify

[17] VREF ←

[17] SYS_TEMP ←

[17] CPU_TEMP ←

[17] PWM_TEMP ←

C11
1u4/X5R/6.3V/K

C12
1u4/X5R/6.3V/K

R42
10K/4/1

R756
8.2K/4

R757
8.2K/4

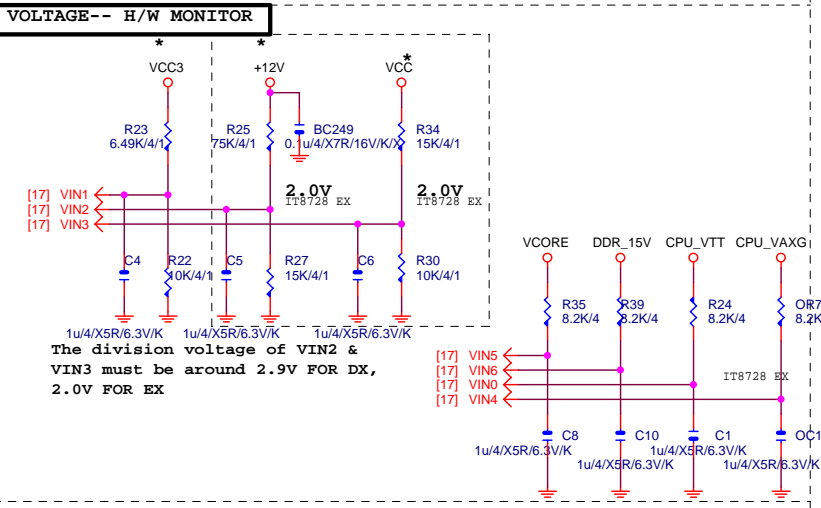
R758
8.2K/4

C232
1u4/X5R/6.3V/K/K

Close SIO

Close CPU

V CORE MOS

[illegible]

The schematic diagram illustrates the CPU Smart Fan control system, divided into two main sections: CPU_FAN and SYS_FAN.

CPU_FAN Section:

- Input:** FANPWM1 (17) is connected to the non-inverting input (pin 2) of the LM358DR/SO8 op-amp (U12A).
- Feedback:** A feedback network consisting of R752 (1K/4/1/X), R754 (22K/4), and BC273 (1u/4/X5R/6.3V/K) is connected to the inverting input (pin 3).
- Output:** The op-amp output (pin 1) drives the base of the P2003ED/P/TO252/30m MOSFET (Q114).
- Power:** The MOSFET is powered by +12V through R750 (22K/4) and R751 (8.2K/4). The drain is connected to the CPU_FAN VCC.
- Protection:** An Anti-Spike protection circuit (Q1, BAV99/SOT23/300mA/X) is connected to the CPU_FAN VCC line.
- Output:** The CPU_FAN output is labeled FANIO1 (17).

SYS_FAN Section:

- Input:** FANPWM2 (17) is connected to the non-inverting input (pin 2) of the LM358DR/SO8 op-amp (U12B).
- Feedback:** A feedback network consisting of R372 (22K/4), R375 (1K/4/1), and BC164 (1u/4/X5R/6.3V/K) is connected to the inverting input (pin 3).
- Output:** The op-amp output (pin 1) drives the base of the P2003ED/P/TO252/30m MOSFET (Q11).
- Power:** The MOSFET is powered by +12V through R357 (8.2K/4) and R171 (8.2K/4). The drain is connected to the SYS_FAN VCC.
- Protection:** A Short Protect circuit (R0402-2) is connected to the SYS_FAN VCC line.
- Output:** The SYS_FAN output is labeled FANIO2 (17).

Common Components:

- Power:** +12V supply is used for both sections.
- Grounding:** Ground connections are shown for various components.
- Capacitors:** Various capacitors (C210, C189, C19, C212, C20) are used for timing and filtering.

FOR EMI ONLY

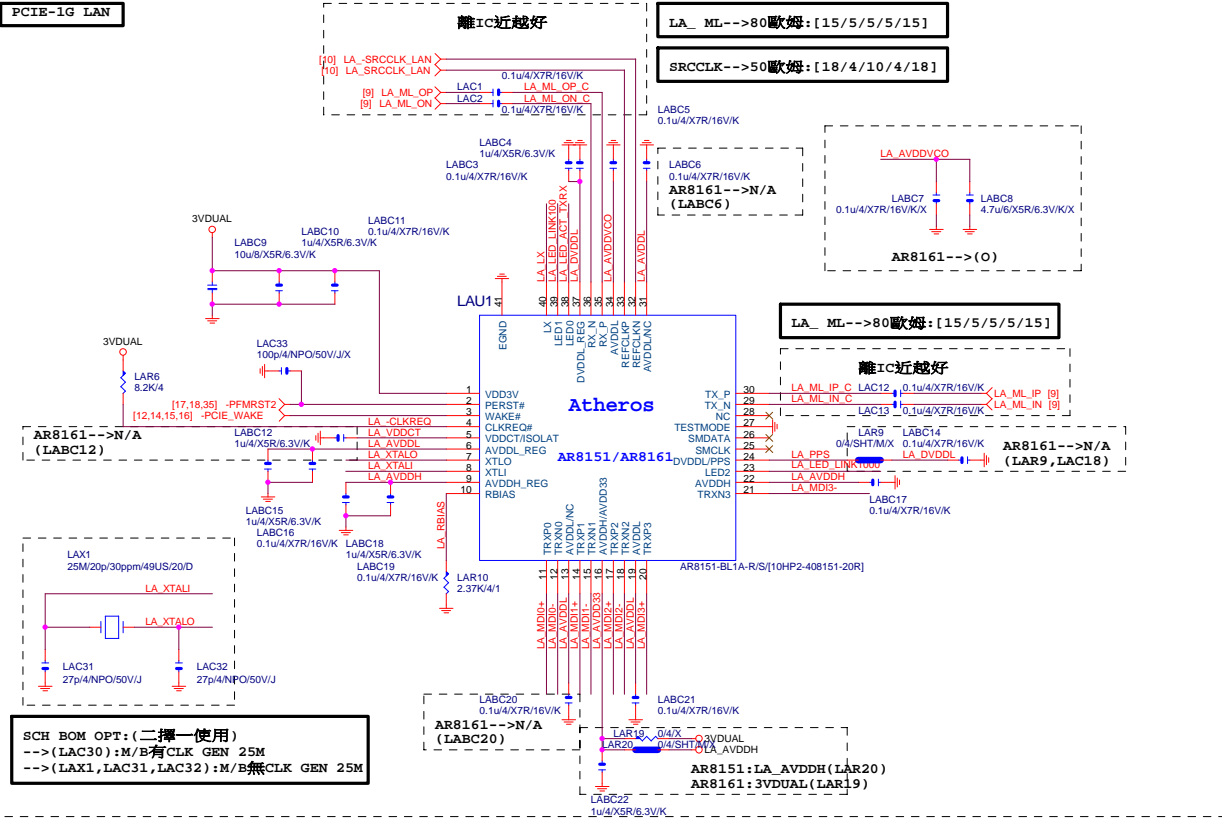
AGND1

R20 0/6/SHT/M/X

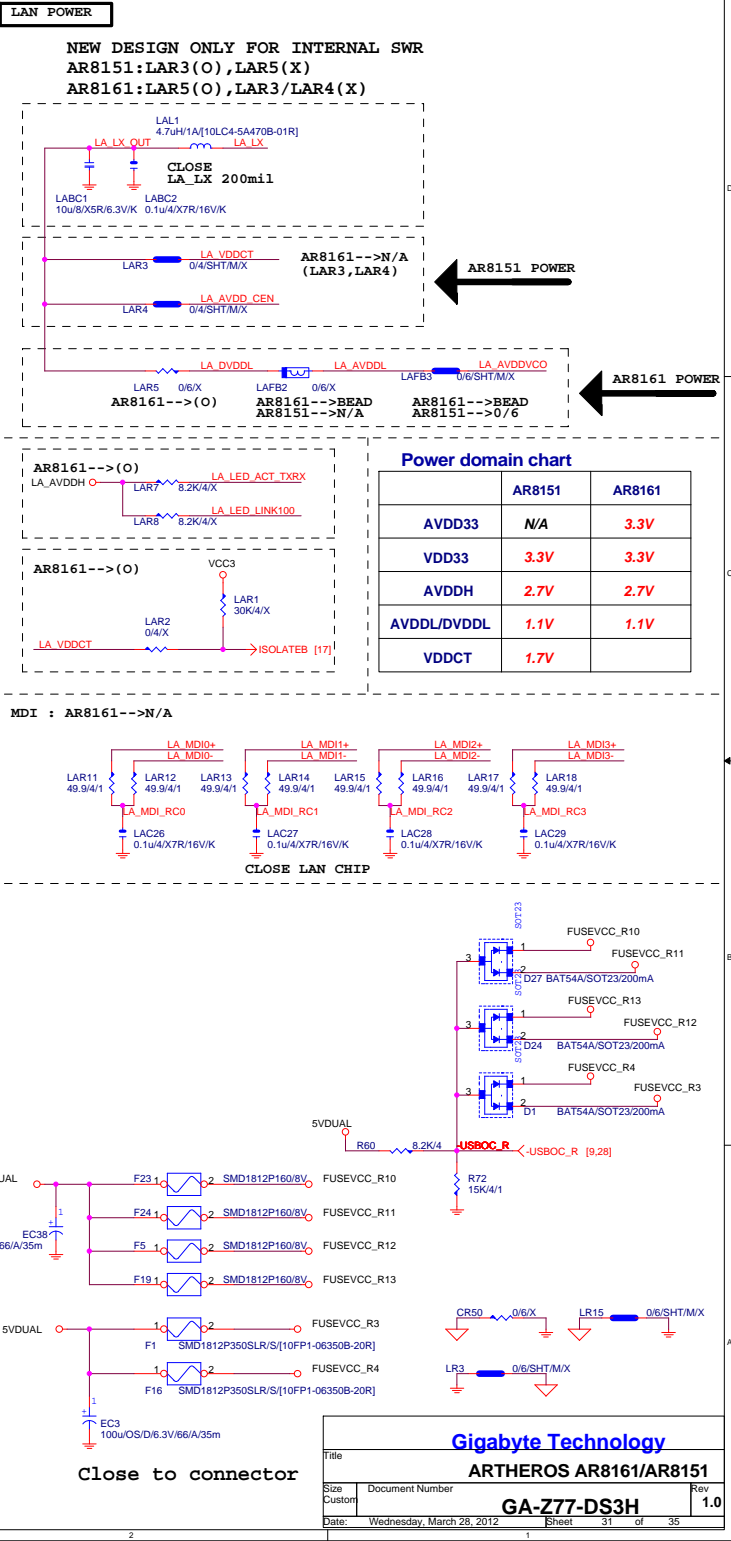
+12V

C21 1n/4/X7R/50V/K

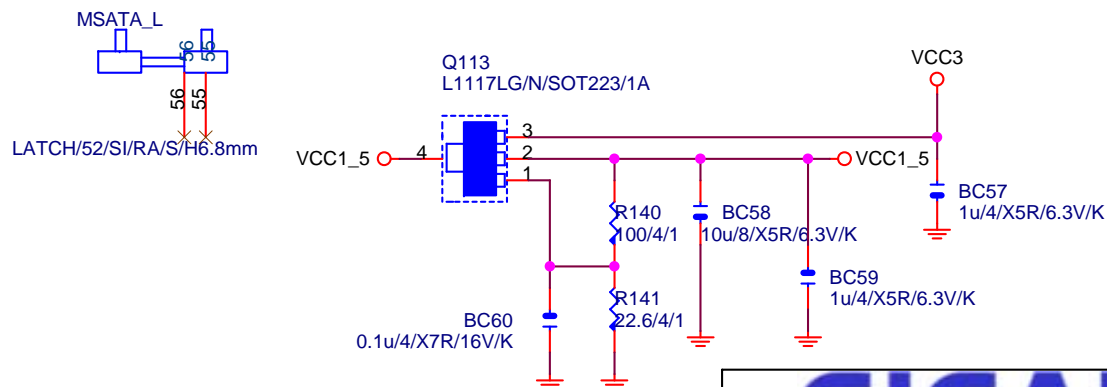
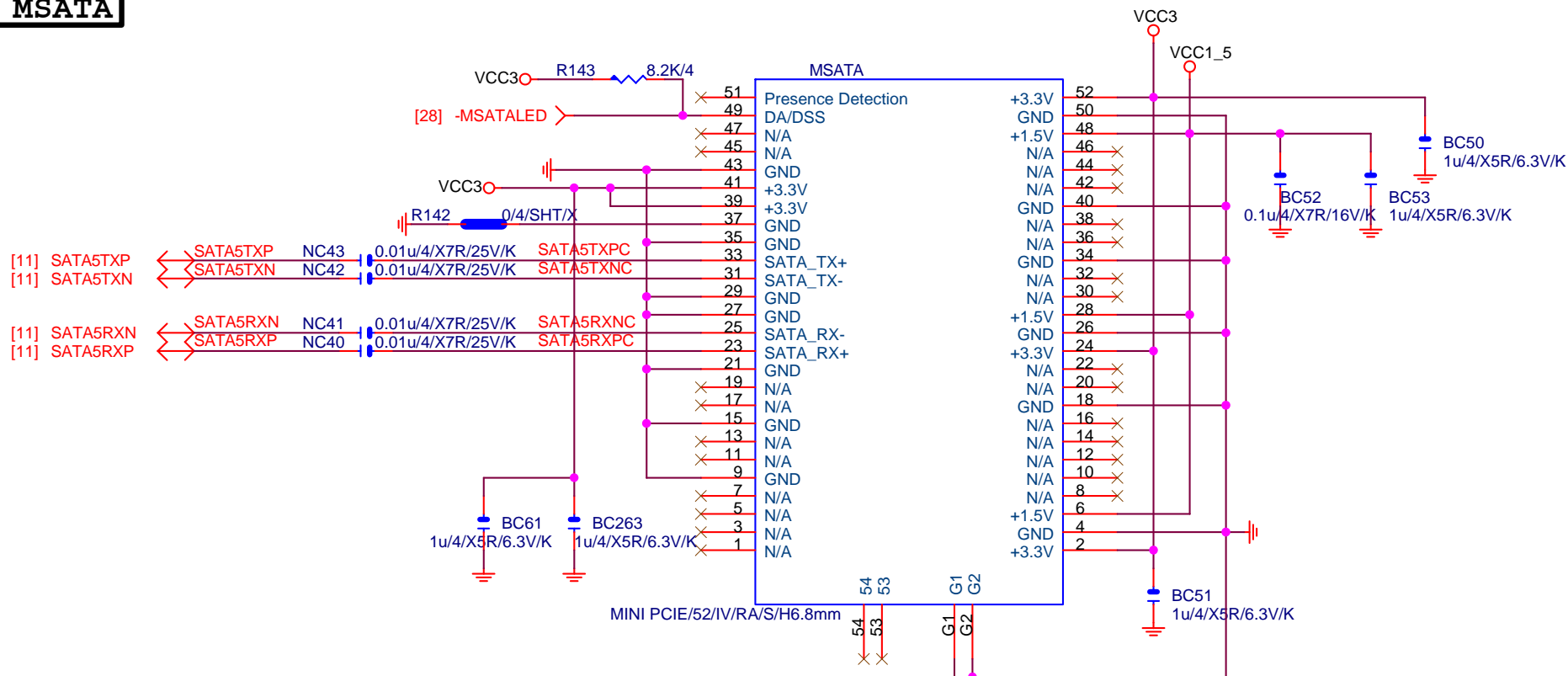
PCIE-1G LAN



LAN POWER

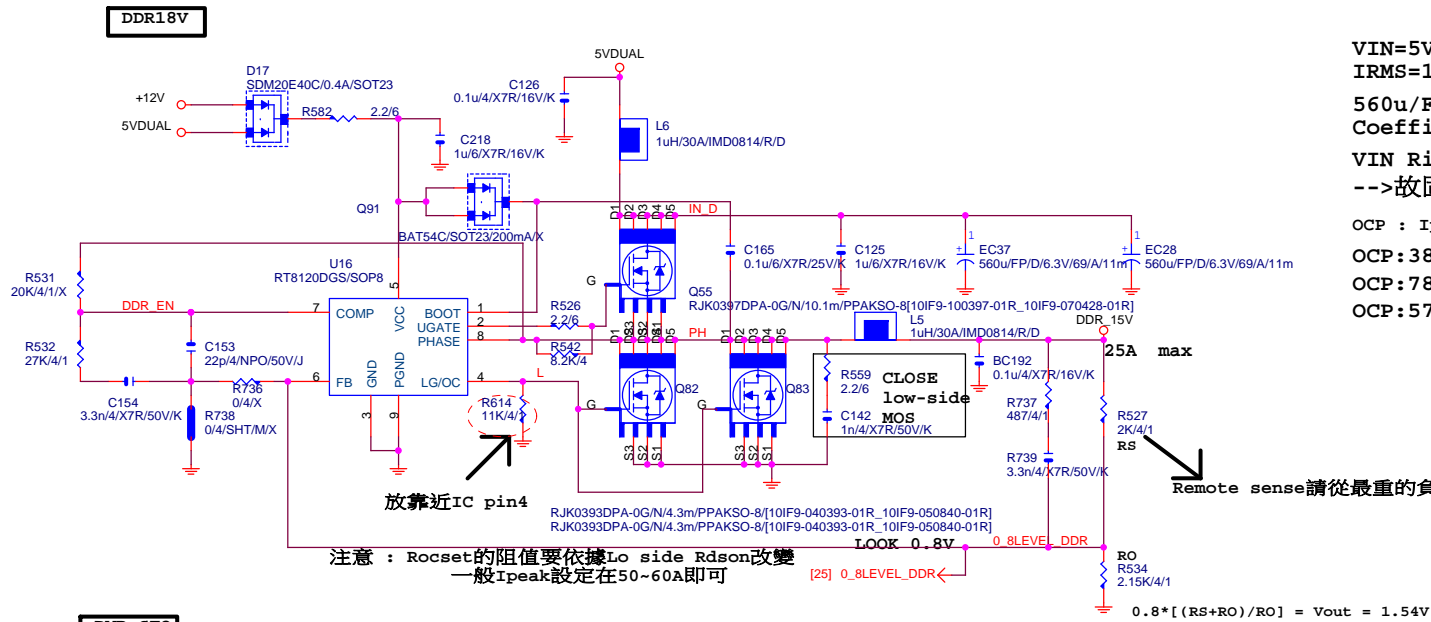


MSATA

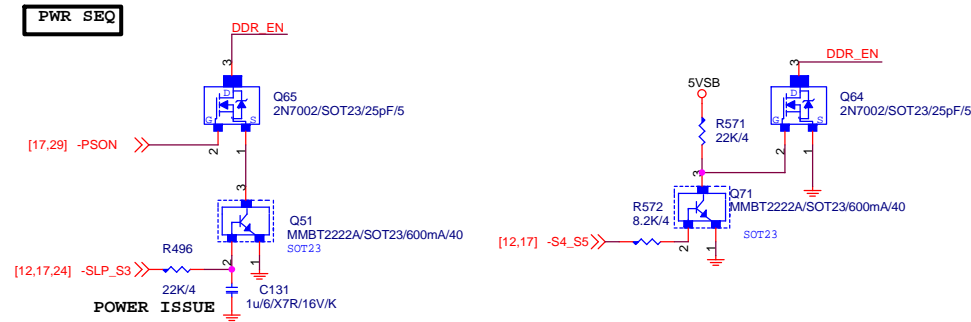


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Title		
MSATA		
Size A	Document Number GA-Z77-DS3H	Rev 1.0
Date:	Wednesday, March 28, 2012	Sheet 32 of 35

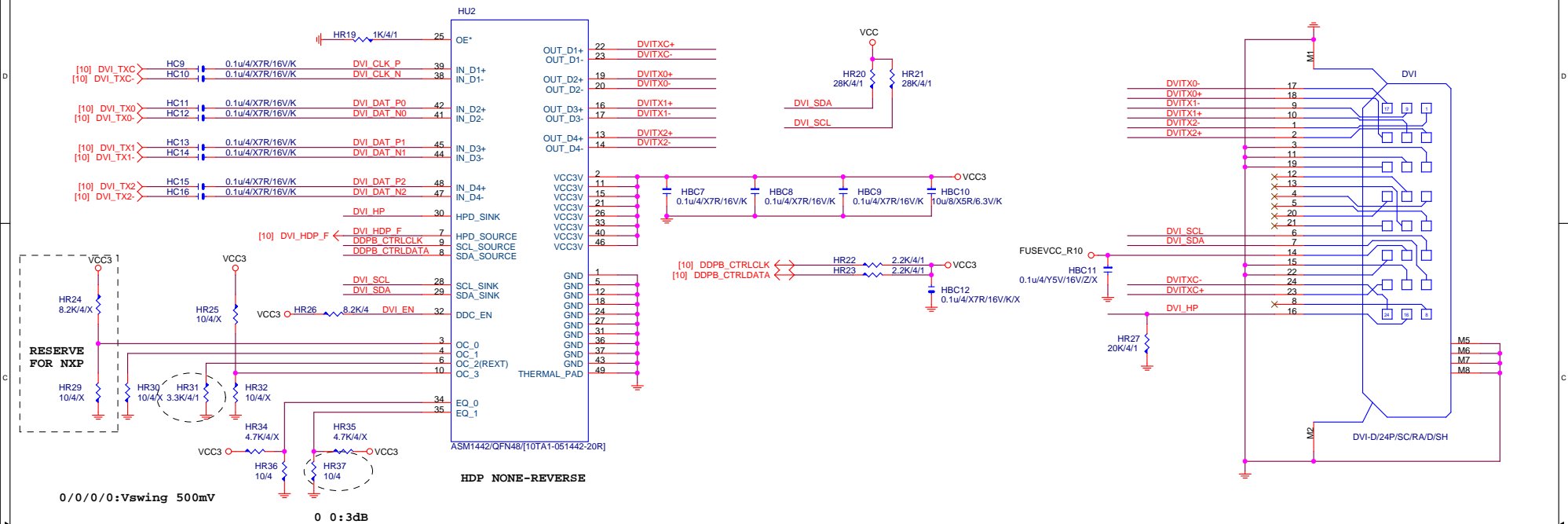


VIN=5V, VOUT=1.5V, IOUT=25A, PHASE=1
IRMS=11.45A
560u/FP/D/6.3V/68/8m RIPPLE CURRENT=4.7A
Coefficient=1.7(85°C), 1(105°C)
VIN Ripple current=4.7X1.7=7.99A(85°C)
-->故固態電容須2X7.99=15.98>11.45A
OCF : $I_{peak} = (2 \times I_{ocset} \times R_{ocset}) / R_{ds(on)}$
OCF: 38.31A for Rds=6.7m for vishay@4.5V
OCF: 78.78A for Rds=3.3m for renesas@10V
OCF: 57A=Rocset*Iocset / Rds(on)
=11K*10uA / [5//5]



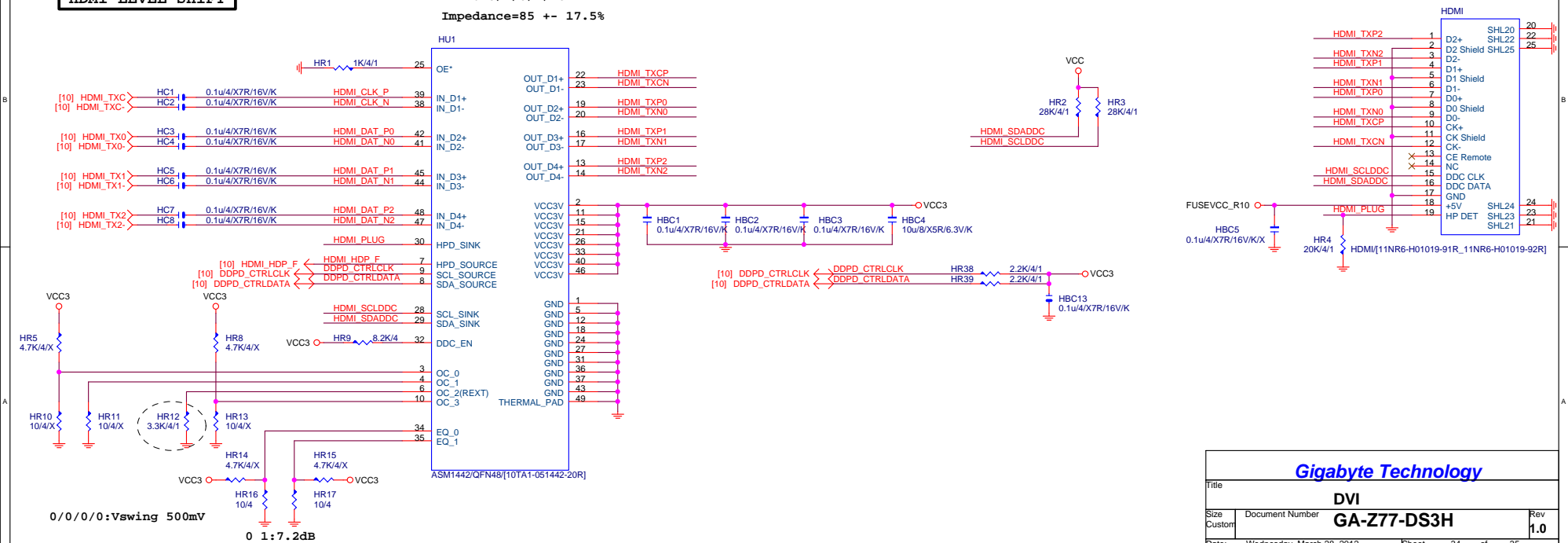
GIGABYTE™			
Title			
RT8120			
Size	Document Number	Rev	
Custom	GA-Z77-DS3H	1.0	
Date:	Wednesday, March 28, 2012	Sheet	33 of 35

DVI LEVEL SHIFT



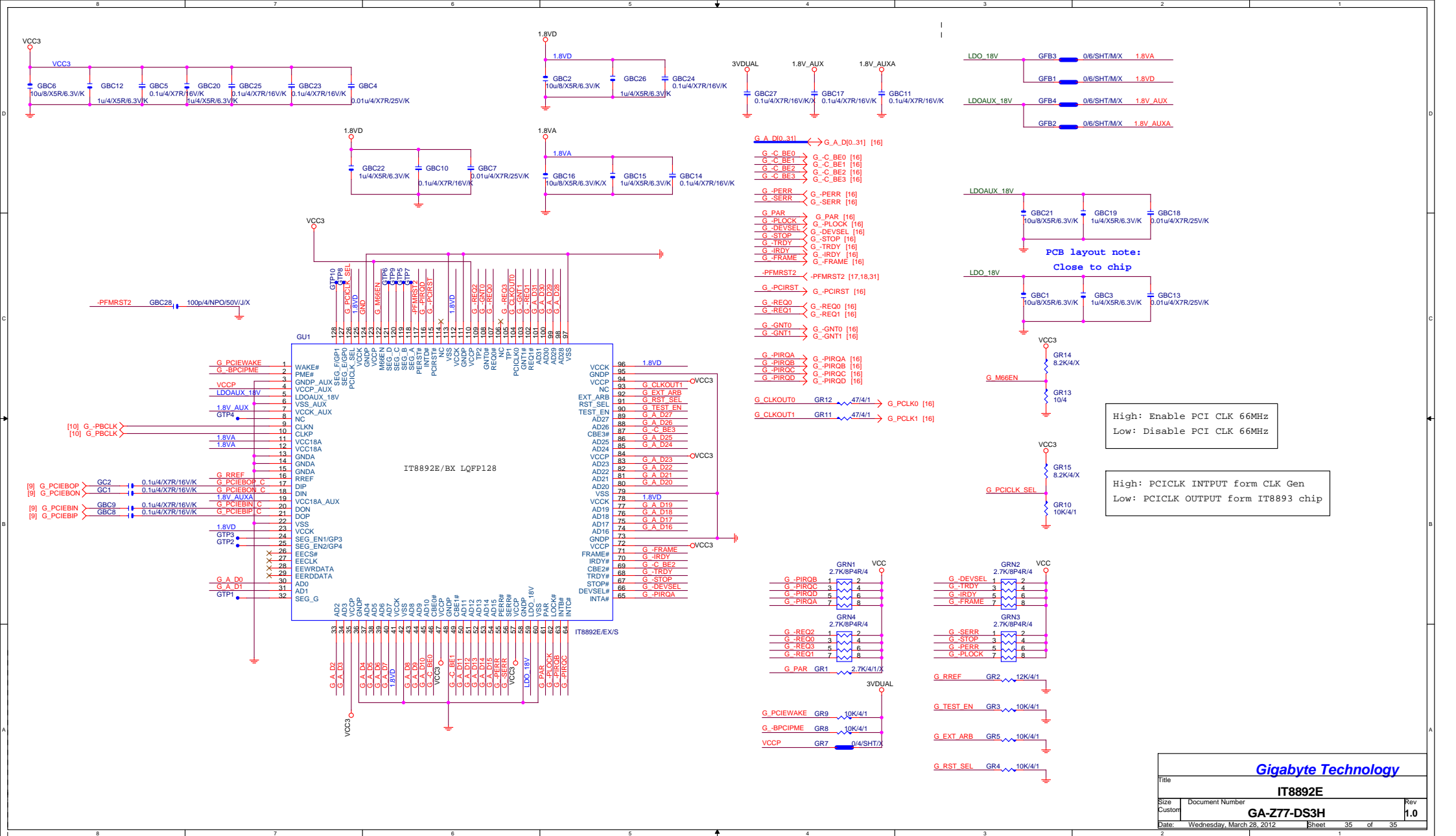
HDMI LEVEL SHIFT
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HDMI:20/4/6/4/20
Impedance=85 +- 17.5%



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Title			
DVI			
Size Custom	Document Number	GA-Z77-DS3H	Rev 1.0
Date:	Wednesday, March 28, 2012	Sheet 34 of 35	



High: Enable PCI CLK 66MHz
Low: Disable PCI CLK 66MHz

High: PCICLK INPUT form CLK Gen
Low: PCICLK OUTPUT form IT8893 chip

Gigabyte Technology			
IT8892E			
Title	Document Number	Rev	1.0
Size	Custom	GA-Z77-DS3H	
Date:	Wednesday, March 28, 2012	Sheet	35 of 35